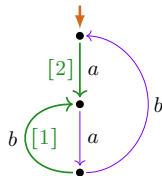
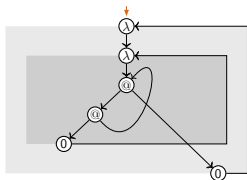


# Modeling Terms by Graphs with Structure Constraints (Two Illustrations)

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Computer Science Seminar  
GSSI  
August 30, 2018



# Overview

## 1. Maximal sharing of functional programs

- ▶ higher-order  $\lambda$ -term graphs

## 2. Process interpretation of regular expressions

- ▶ LEE-witnesses: graph labelings based on a loop-condition LEE

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- ▶ from terms in the  $\lambda$ -calculus with letrec to:
  - ▶ higher-order  $\lambda$ -term graphs
  - ▶ first-order  $\lambda$ -term graphs
  - ▶  $\lambda$ -NFAs, and  $\lambda$ -DFAs
- ▶ minimization / readback / efficiency / Haskell implementation

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- ▶ Milner's questions, known results
- ▶ structure-constrained process graphs:
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- ▶ Comparison results

# Comparison desiderata

$\lambda$ -calculus with letrec under unfolding semantics

*Not available:* term graph interpretation that is studied under  $\Leftrightarrow$

- ▶ graph representations used by compilers  
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Regular expressions under process semantics (bisimilarity  $\Leftrightarrow$ )

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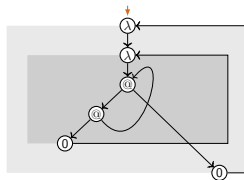
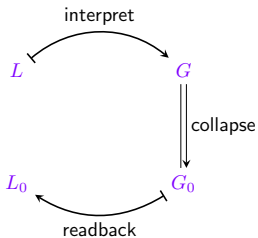
- ▶ **not closed** under  $\Rightarrow$ , and  $\Leftrightarrow$ , modulo  $\Leftrightarrow$  incomplete

*Desired:* reason with graphs that are  $\llbracket \cdot \rrbracket_{\mathcal{P}}$ -expressible modulo  $\Leftrightarrow$   
(at least with 'sufficiently many')

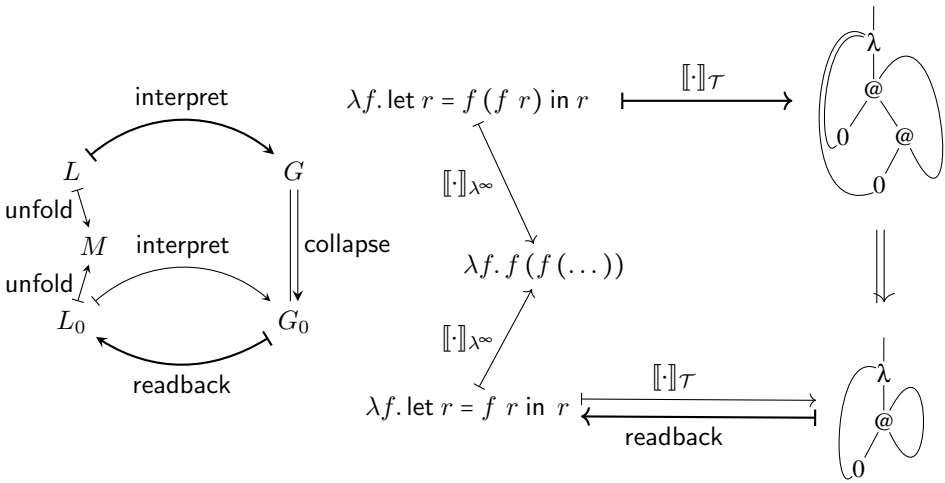
understand incompleteness by a structural graph property

# Maximal sharing of functional programs

(joint work with Jan Rochel)



# maximal sharing: example (fix)



# maximal sharing: the method

$$L \xrightarrow{[[\cdot]]_{\mathcal{H}}} \mathcal{G}$$

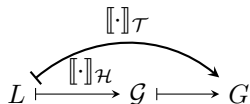
1. term graph interpretation  $[[\cdot]]$ .  
of  $\lambda_{\text{letrec}}$ -term  $L$  as:
  - a. higher-order term graph  
 $\mathcal{G} = [[L]]_{\mathcal{H}}$

# maximal sharing: the method

$$L \xrightarrow{\llbracket \cdot \rrbracket_{\mathcal{H}}} \mathcal{G} \longrightarrow G$$

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# maximal sharing: the method



## 1. term graph interpretation $[[\cdot]]$ .

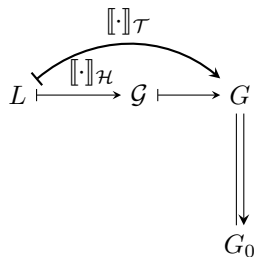
of  $\lambda_{\text{letrec}}$ -term  $L$  as:

a. **higher-order** term graph

$$G = [[L]]_{\mathcal{H}}$$

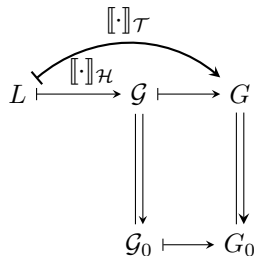
b. **first-order** term graph  $G = [[L]]_{\mathcal{T}}$

# maximal sharing: the method



1. **term graph interpretation**  $[[\cdot]]$ .  
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2. **bisimulation collapse**  $\Downarrow$   
of f-o term graph  $G$  into  $G_0$

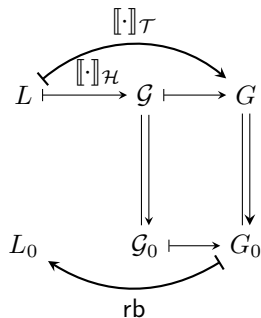
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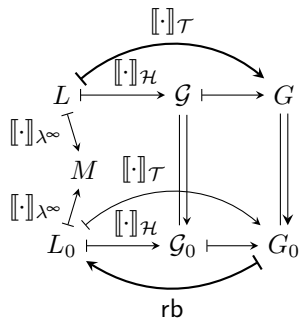


# maximal sharing: the method



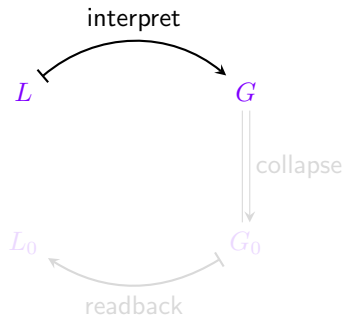
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# interpretation



# running example

instead of:

$$\lambda f. \text{let } r = f (f r) \text{ in } r \quad \longmapsto_{\text{max-sharing}} \quad \lambda f. \text{let } r = f r \text{ in } r$$

we use:

$$\lambda x. \lambda f. \text{let } r = f (f r x) x \text{ in } r \quad \longmapsto_{\text{max-sharing}} \quad \lambda x. \lambda f. \text{let } r = f r x \text{ in } r$$

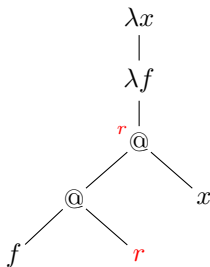
$$L \quad \longmapsto_{\text{max-sharing}} \quad L_0$$

# graph interpretation (example 1)

$$L_0 = \lambda x. \lambda f. \text{let } r = f r x \text{ in } r$$

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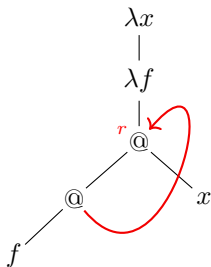
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syntax tree

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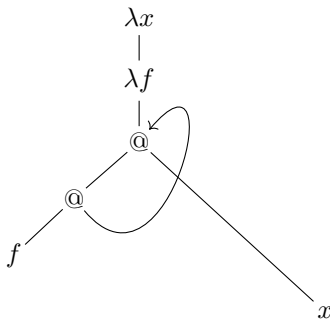
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syntax tree (+ recursive backlink)

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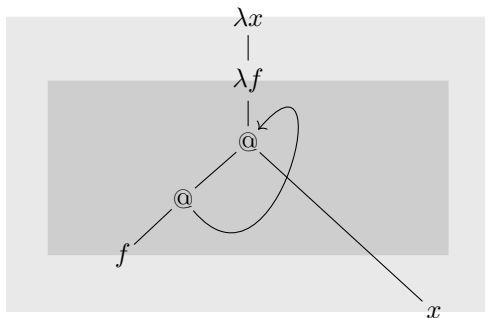


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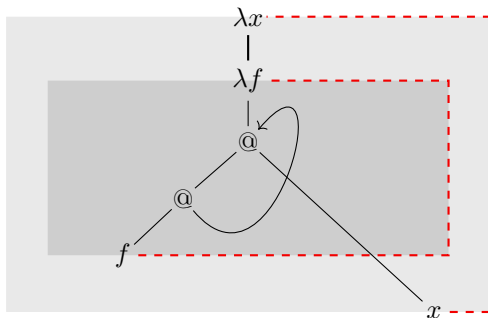
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syntax tree (+ recursive backlink, + scopes)

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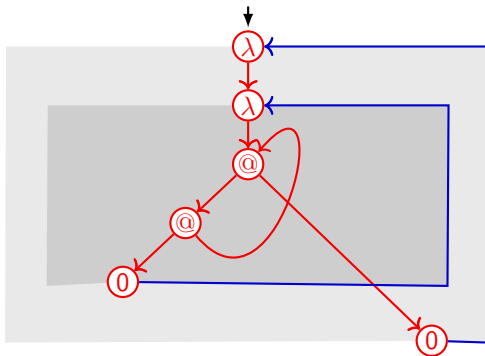
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syntax tree (+ recursive backlink, + scopes, + **binding links**)

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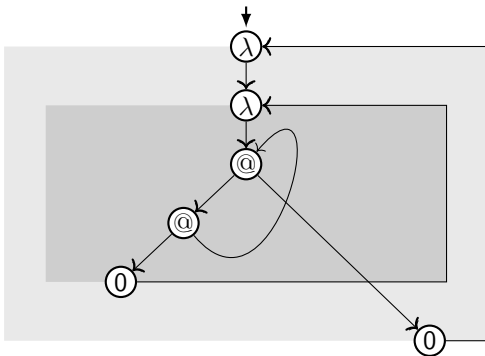
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first-order term graph with binding backlinks (+ scope sets)

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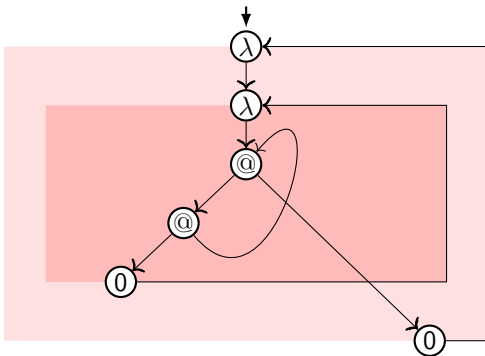
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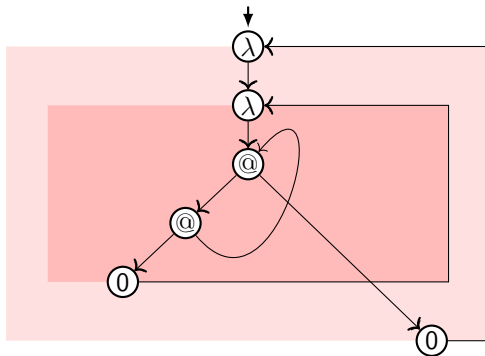
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first-order term graph (+ **scope sets**)

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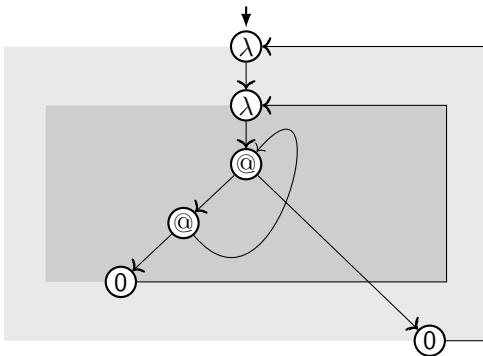
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higher-order term graph (with scope sets, Blom [2003])

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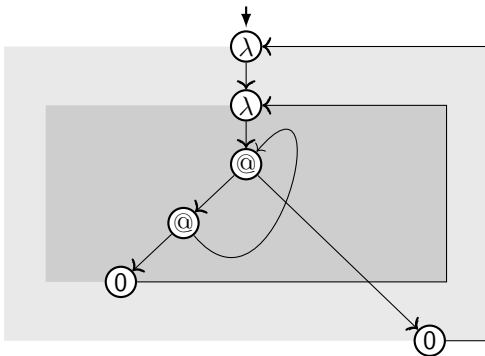
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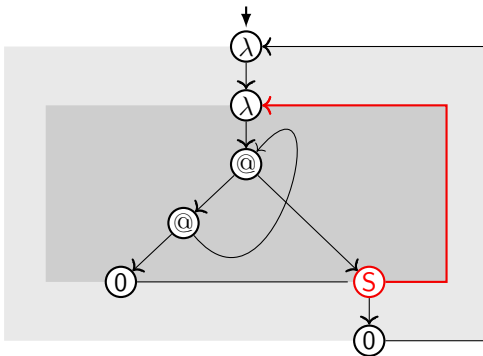


first-order term graph with binding backlinks (+ scope sets)



# graph interpretation (example 1)

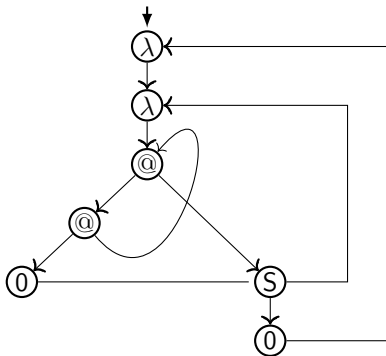
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first-order term graph with **scope vertices with backlinks** (+ scope sets)

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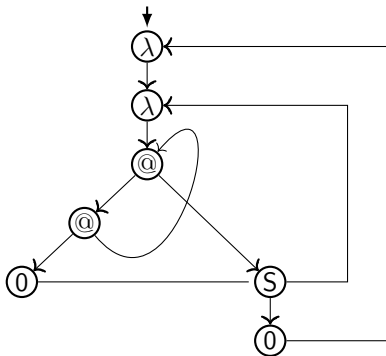
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first-order term graph with scope vertices with backlinks

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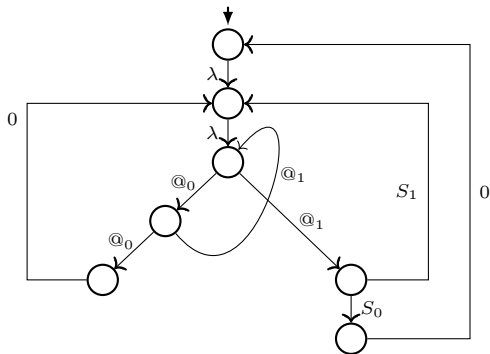
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$\lambda$ -term-graph  $\llbracket L_0 \rrbracket_{\mathcal{T}}$

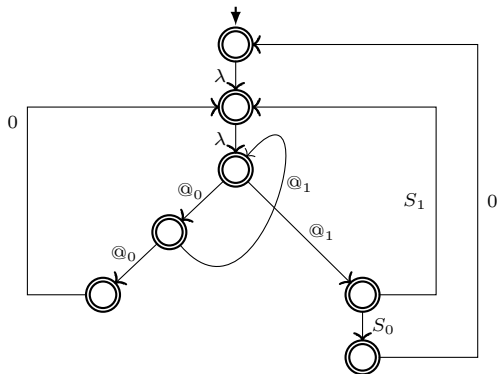
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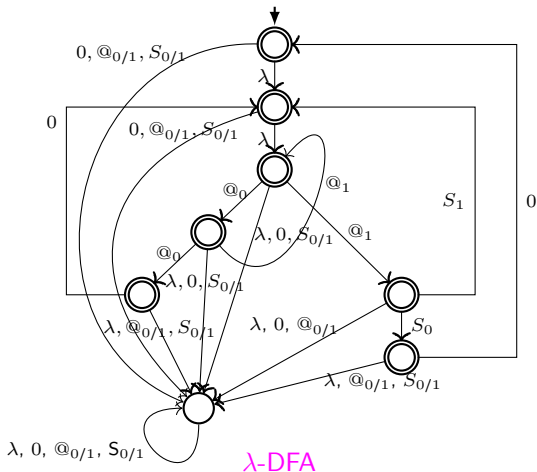
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λ-NFA

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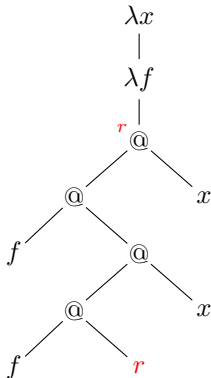


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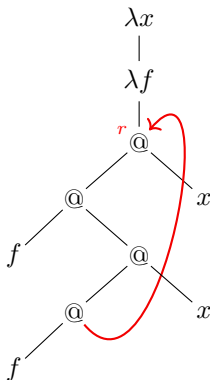


syntax tree



# graph interpretation (example 2)

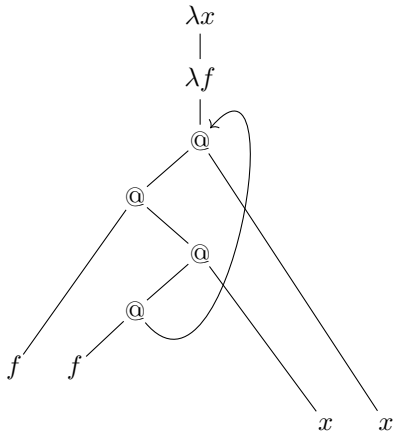
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syntax tree (+ recursive backlink)

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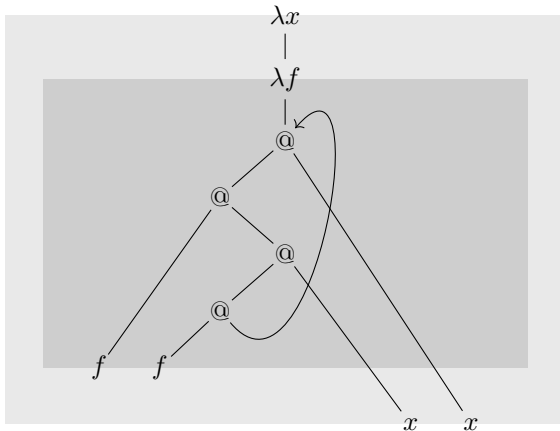
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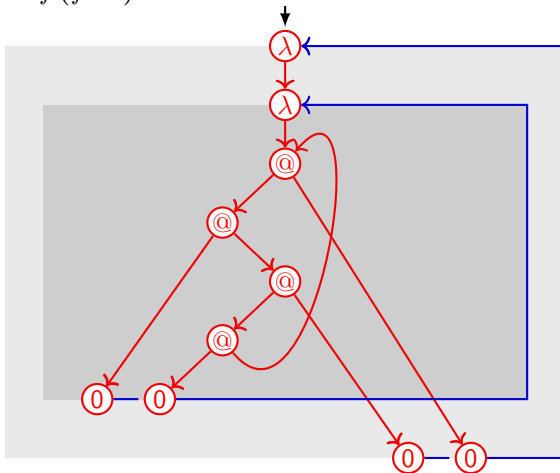
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syntax tree (+ recursive backlink, + scopes)

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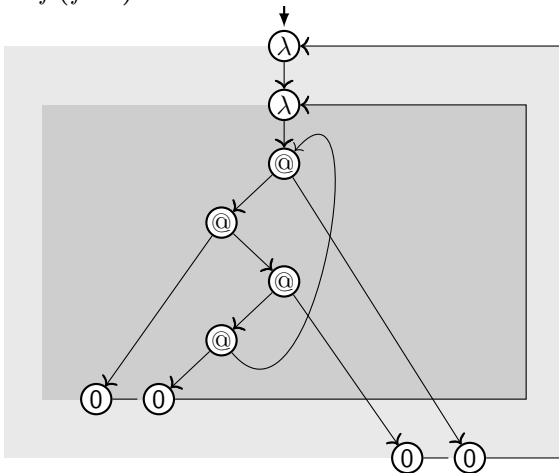
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first-order term graph with binding backlinks (+ scope sets)

# graph interpretation (example 2)

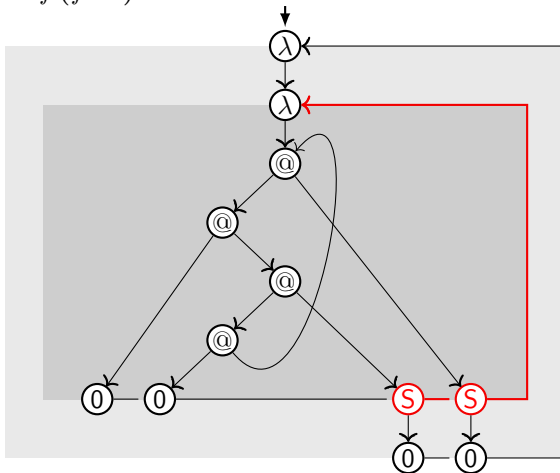
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$\lambda$ -higher-order-term-graph  $\llbracket L \rrbracket_{\mathcal{H}}$

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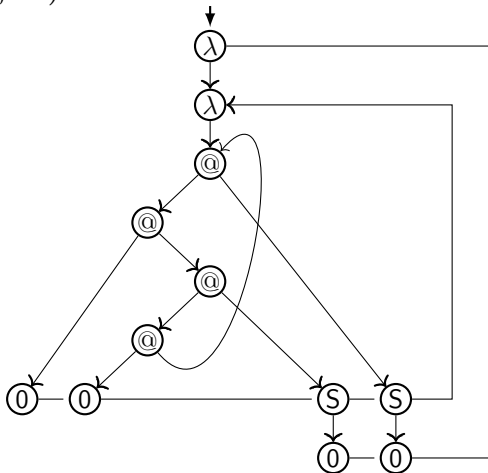
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first-order term graph with **scope vertices with backlinks** (+ scope sets)

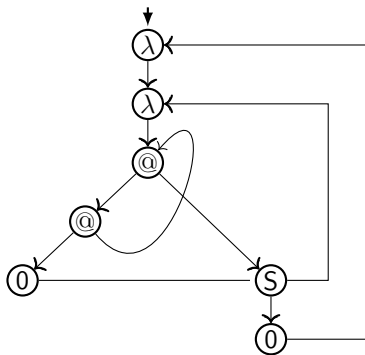
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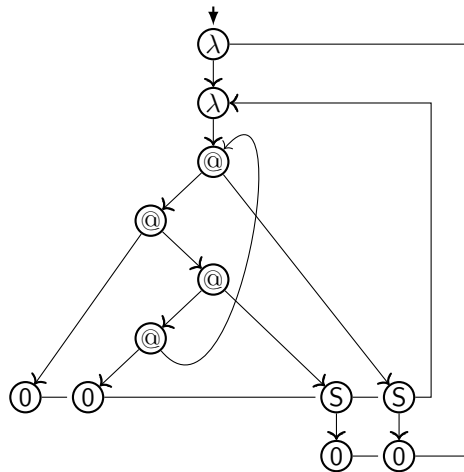


$\lambda$ -term-graph  $\llbracket L \rrbracket_{\tau}$

# graph interpretation (examples 1 and 2)



$\llbracket L_0 \rrbracket_{\mathcal{T}}$



$\llbracket L \rrbracket_{\mathcal{T}}$



# interpretation $[[\cdot]]_{\mathcal{T}}$ : properties (cont.)

interpretation  $\lambda_{\text{letrec}}\text{-term } L \mapsto \lambda\text{-term-graph } [[L]]_{\mathcal{T}}$

- ▶ defined by induction on structure of  $L$
- ▶ similar analysis as fully-lazy lambda-lifting
- ▶ yields **eager-scope  $\lambda$ -term-graphs**:  $\sim$  minimal scopes

## Theorem

For  $\lambda_{\text{letrec}}$ -terms  $L_1$  and  $L_2$  it holds: Equality of infinite unfolding coincides with bisimilarity of  $\lambda$ -term-graph interpretations:

$$[[L_1]]_{\lambda^\infty} = [[L_2]]_{\lambda^\infty} \iff [[L_1]]_{\mathcal{T}} \Leftrightarrow [[L_2]]_{\mathcal{T}}$$

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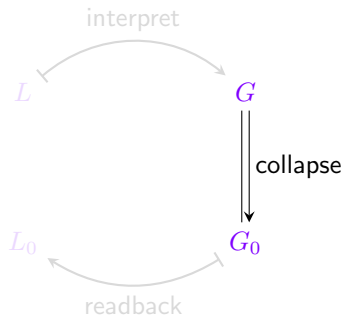
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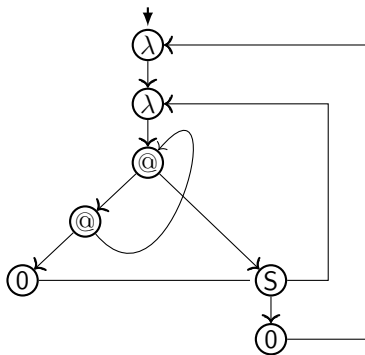
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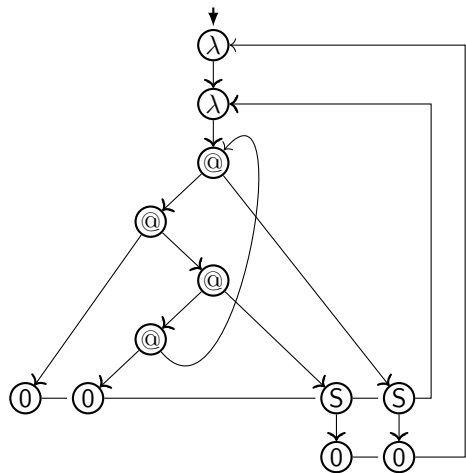
# collapse



# bisimulation check between $\lambda$ -term-graphs

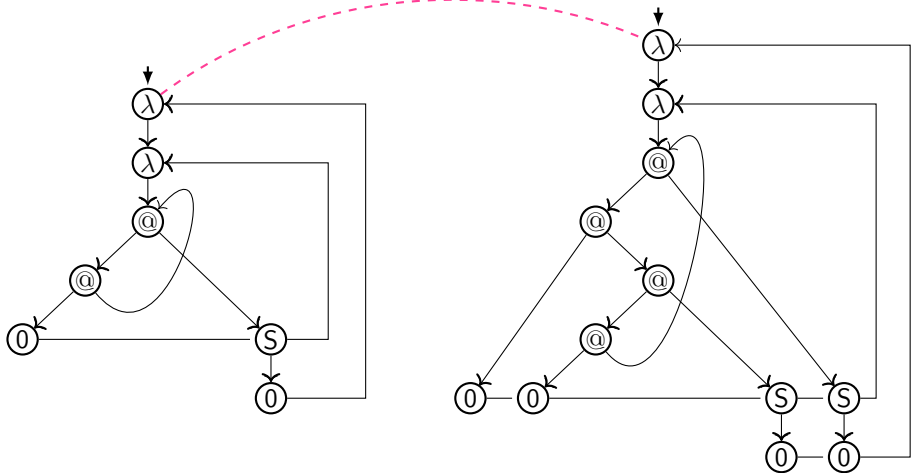


$\llbracket L_0 \rrbracket_{\mathcal{T}}$



$\llbracket L \rrbracket_{\mathcal{T}}$

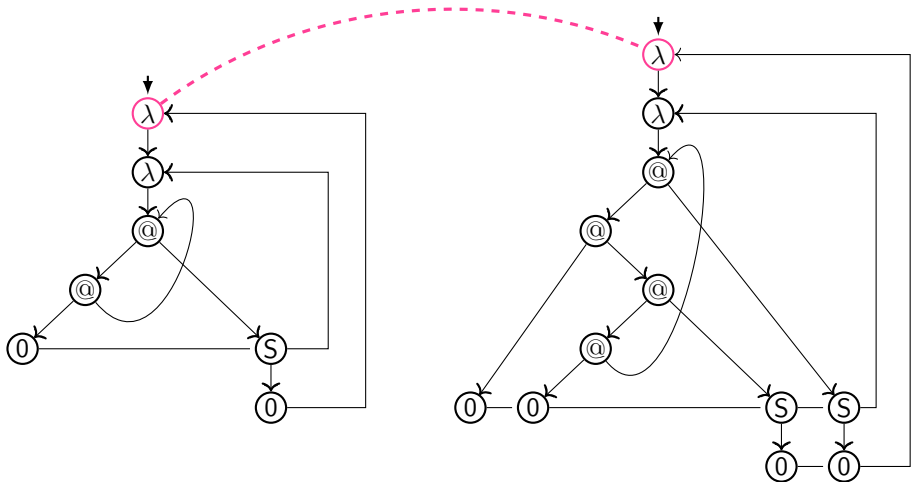
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$\llbracket L_0 \rrbracket_{\mathcal{T}}$

$\llbracket L \rrbracket_{\mathcal{T}}$

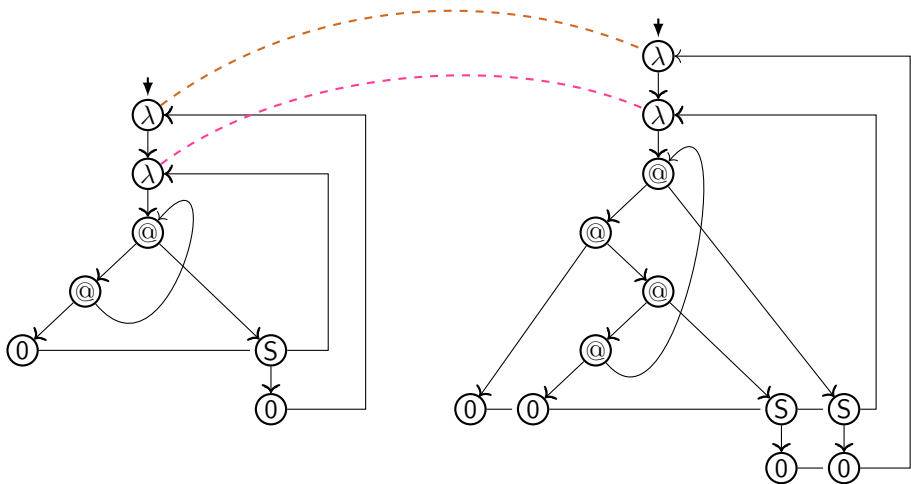
# bisimulation check between $\lambda$ -term-graphs



$\llbracket L_0 \rrbracket_{\mathcal{T}}$

$\llbracket L \rrbracket_{\mathcal{T}}$

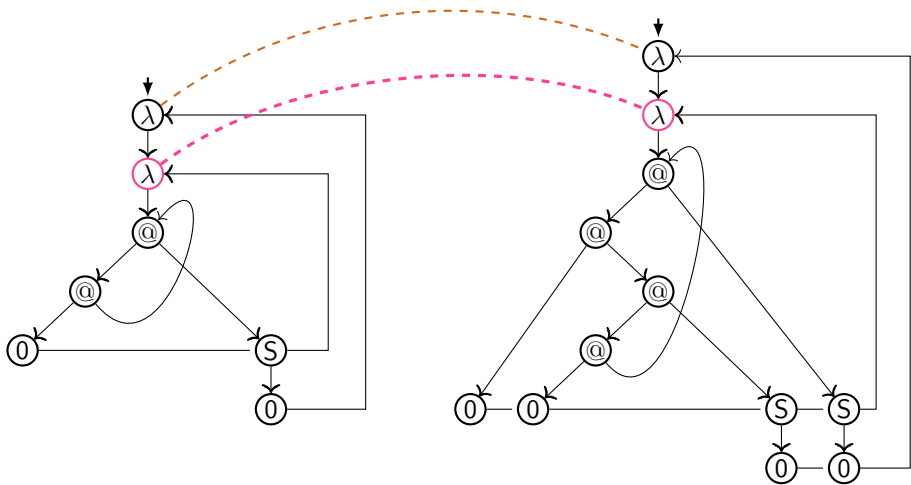
# bisimulation check between $\lambda$ -term-graphs



$\llbracket L_0 \rrbracket_{\mathcal{T}}$

$\llbracket L \rrbracket_{\mathcal{T}}$

# bisimulation check between $\lambda$ -term-graphs

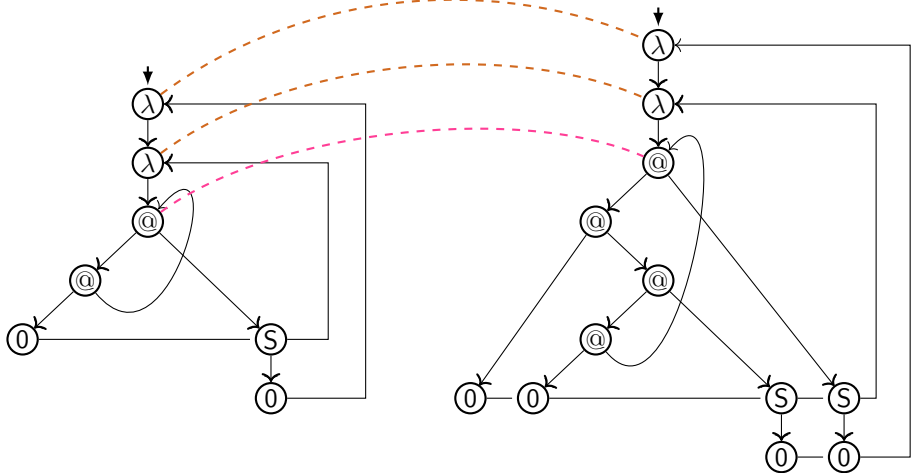


$\llbracket L_0 \rrbracket_{\mathcal{T}}$

$\llbracket L \rrbracket_{\mathcal{T}}$



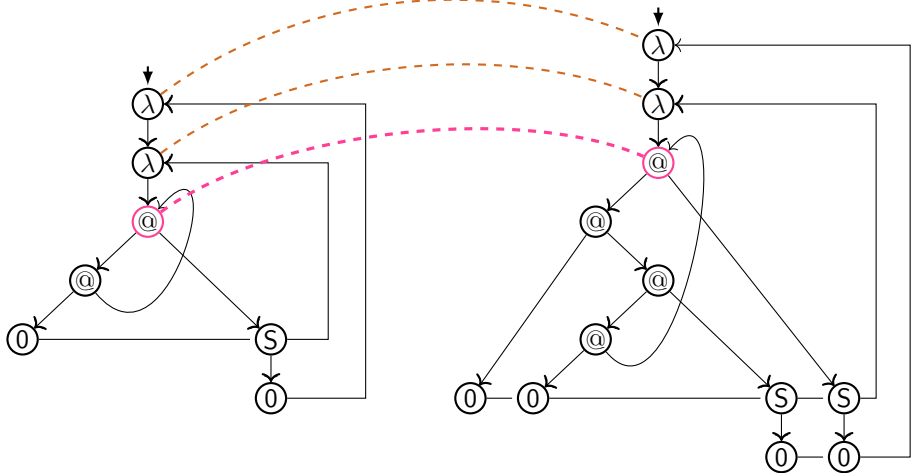
# bisimulation check between $\lambda$ -term-graphs



$\llbracket L_0 \rrbracket_{\mathcal{T}}$

$\llbracket L \rrbracket_{\mathcal{T}}$

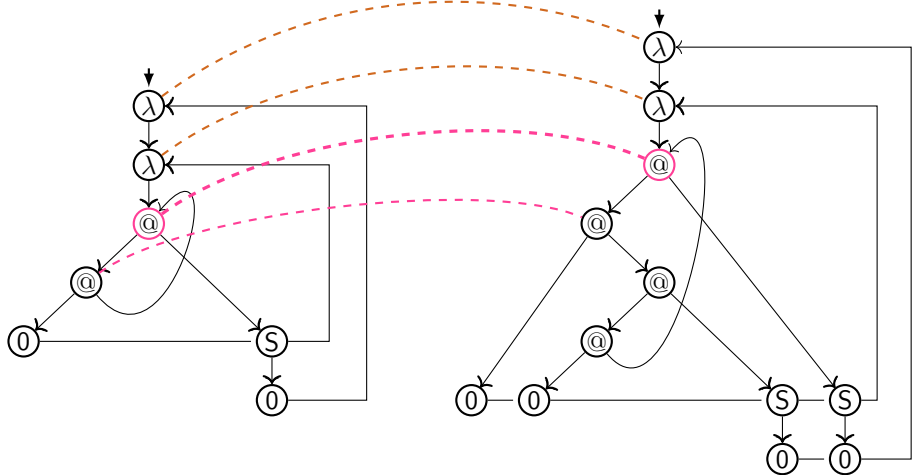
# bisimulation check between $\lambda$ -term-graphs



$\llbracket L_0 \rrbracket_{\mathcal{T}}$

$\llbracket L \rrbracket_{\mathcal{T}}$

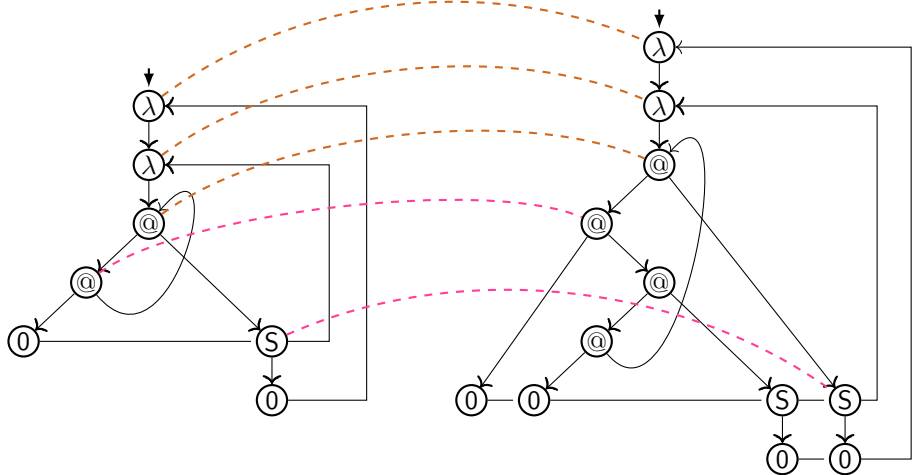
# bisimulation check between $\lambda$ -term-graphs



$\llbracket L_0 \rrbracket_{\mathcal{T}}$

$\llbracket L \rrbracket_{\mathcal{T}}$

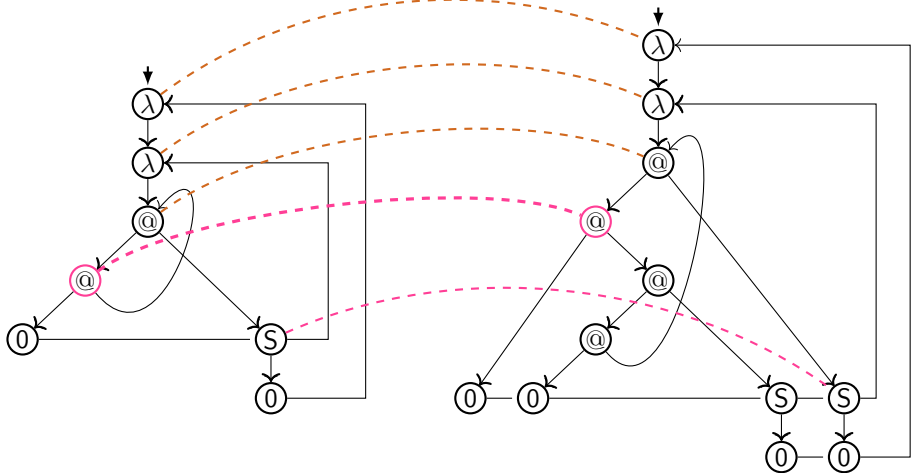
# bisimulation check between $\lambda$ -term-graphs



$\llbracket L_0 \rrbracket_{\mathcal{T}}$

$\llbracket L \rrbracket_{\mathcal{T}}$

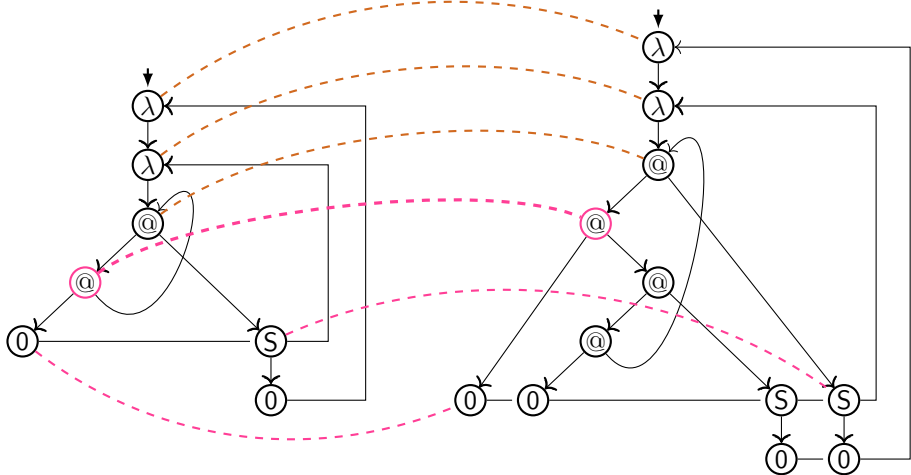
# bisimulation check between $\lambda$ -term-graphs



$\llbracket L_0 \rrbracket_{\mathcal{T}}$

$\llbracket L \rrbracket_{\mathcal{T}}$

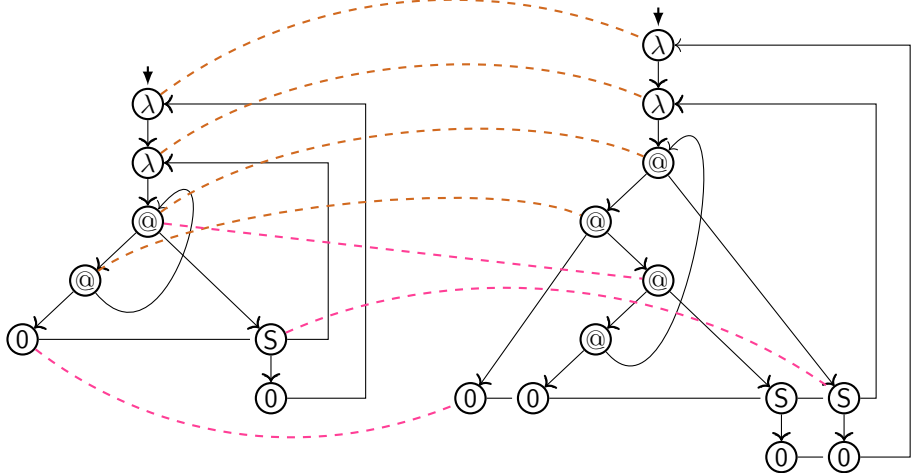
# bisimulation check between $\lambda$ -term-graphs



$\llbracket L_0 \rrbracket_{\mathcal{T}}$

$\llbracket L \rrbracket_{\mathcal{T}}$

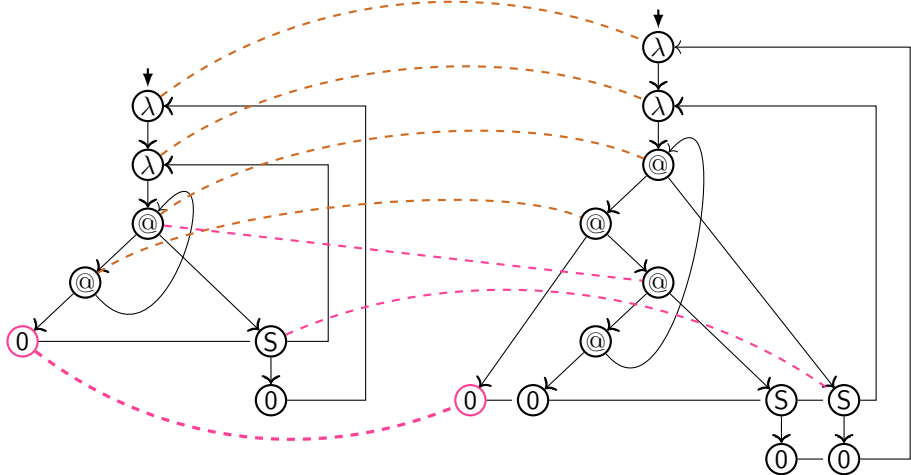
# bisimulation check between $\lambda$ -term-graphs



$\llbracket L_0 \rrbracket_{\mathcal{T}}$

$\llbracket L \rrbracket_{\mathcal{T}}$

# bisimulation check between $\lambda$ -term-graphs

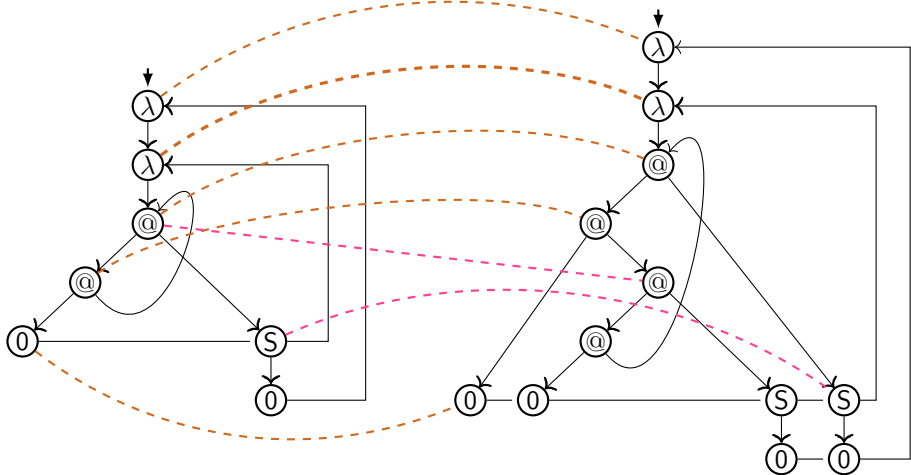


$\llbracket L_0 \rrbracket_{\mathcal{T}}$

$\llbracket L \rrbracket_{\mathcal{T}}$



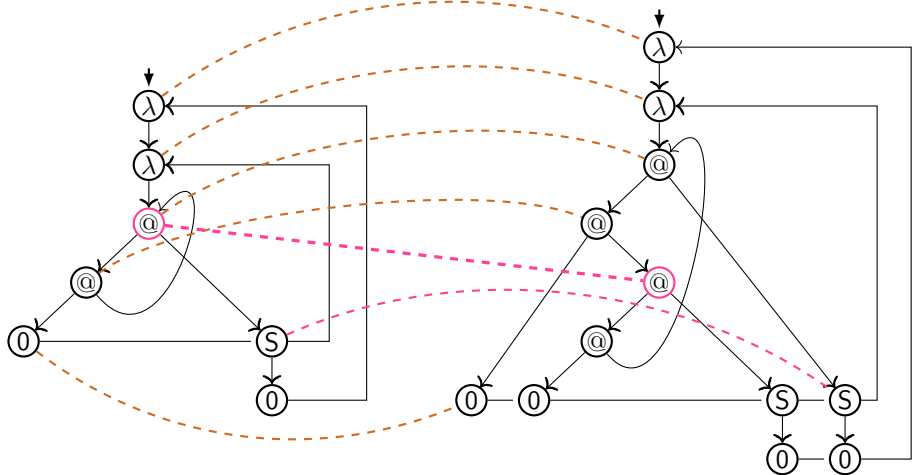
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$\llbracket L_0 \rrbracket_{\mathcal{T}}$

$\llbracket L \rrbracket_{\mathcal{T}}$

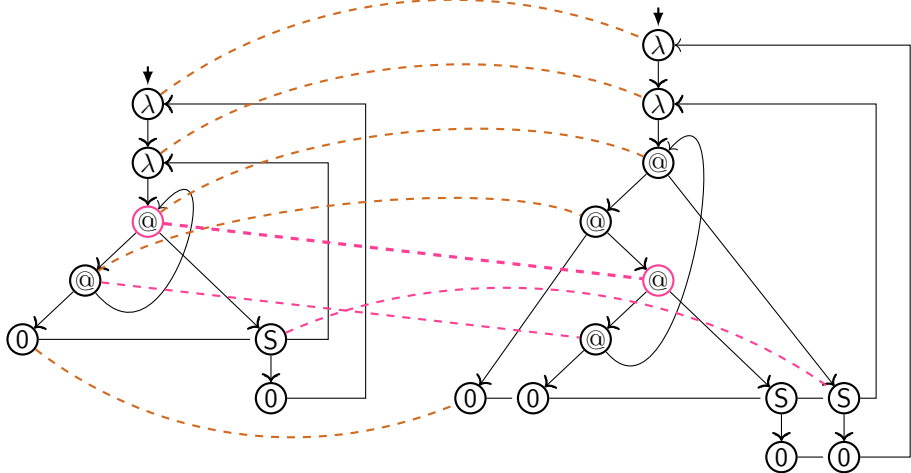
# bisimulation check between $\lambda$ -term-graphs



$\llbracket L_0 \rrbracket_{\mathcal{T}}$

$\llbracket L \rrbracket_{\mathcal{T}}$

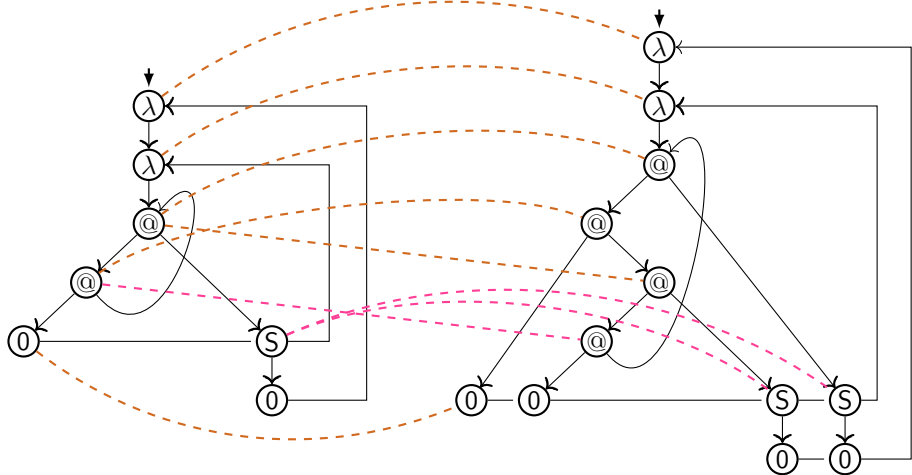
# bisimulation check between $\lambda$ -term-graphs



$\llbracket L_0 \rrbracket_{\mathcal{T}}$

$\llbracket L \rrbracket_{\mathcal{T}}$

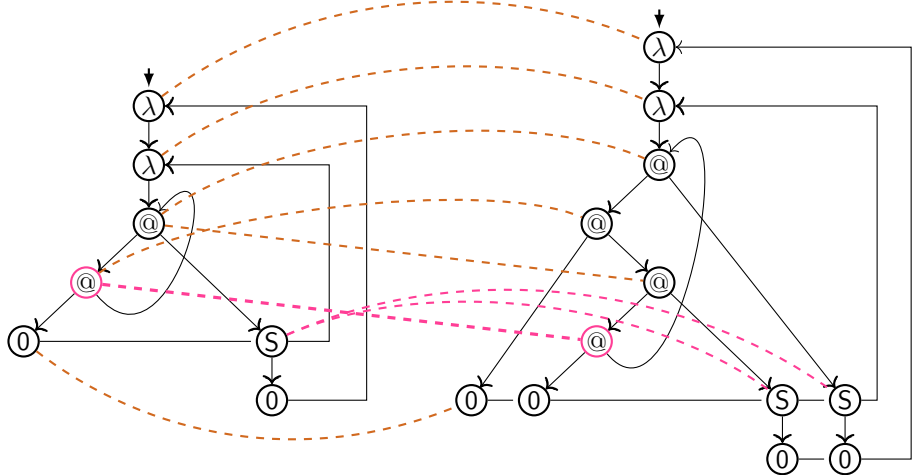
# bisimulation check between $\lambda$ -term-graphs



$\llbracket L_0 \rrbracket_{\mathcal{T}}$

$\llbracket L \rrbracket_{\mathcal{T}}$

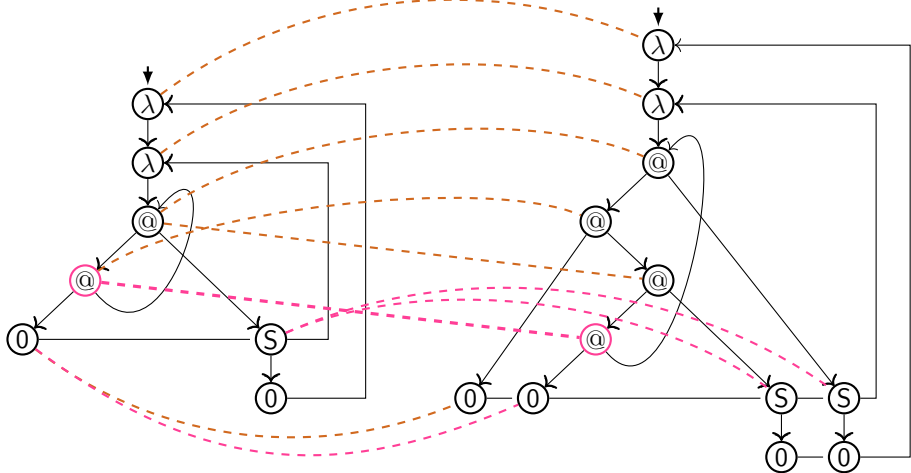
# bisimulation check between $\lambda$ -term-graphs



$\llbracket L_0 \rrbracket_{\mathcal{T}}$

$\llbracket L \rrbracket_{\mathcal{T}}$

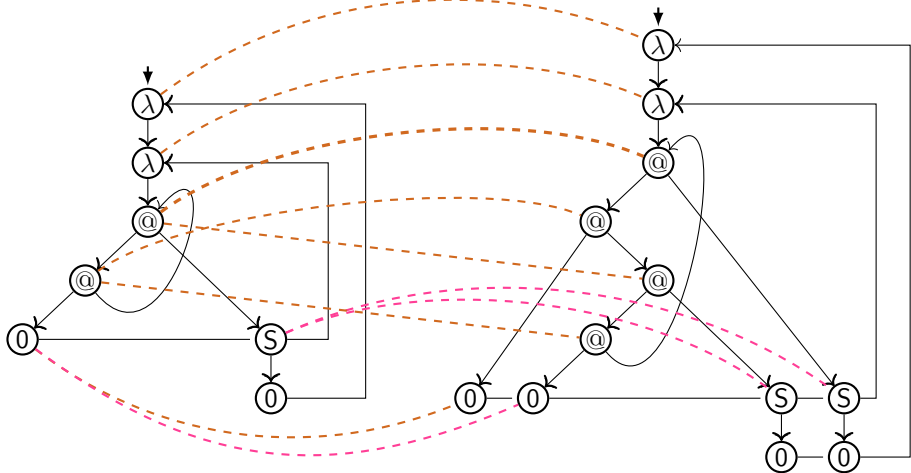
# bisimulation check between $\lambda$ -term-graphs



$\llbracket L_0 \rrbracket_{\mathcal{T}}$

$\llbracket L \rrbracket_{\mathcal{T}}$

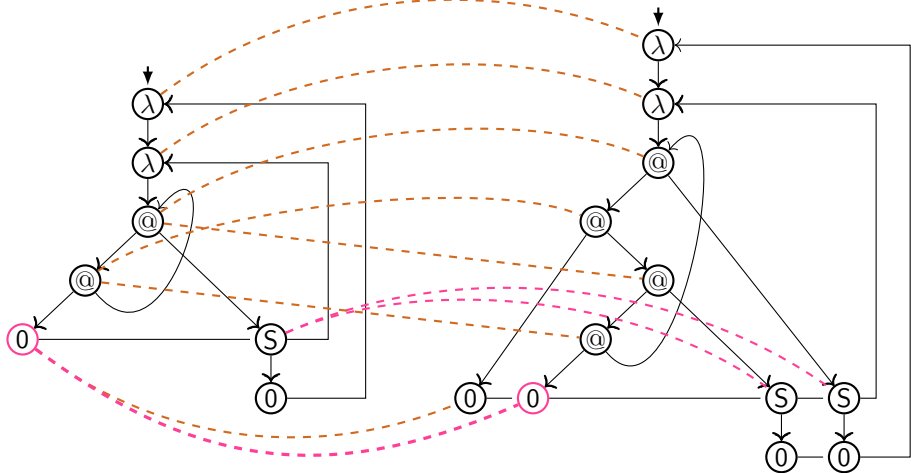
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$\llbracket L_0 \rrbracket_{\mathcal{T}}$

$\llbracket L \rrbracket_{\mathcal{T}}$

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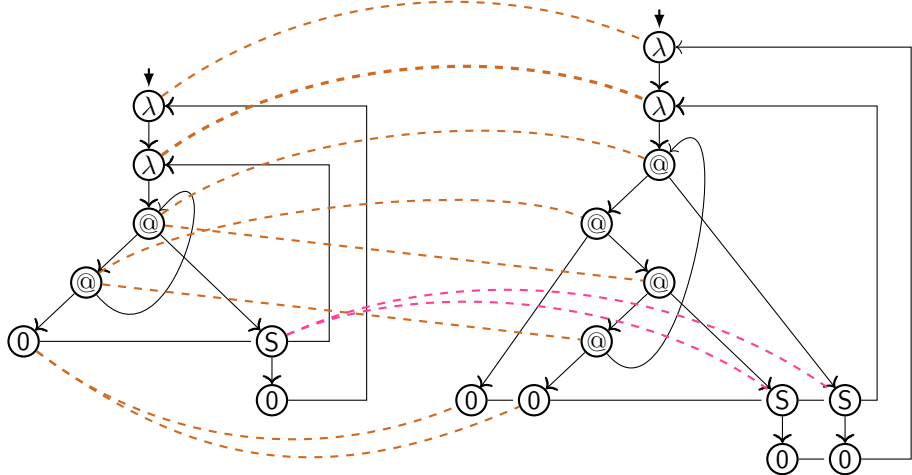


$\llbracket L_0 \rrbracket_{\mathcal{T}}$

$\llbracket L \rrbracket_{\mathcal{T}}$



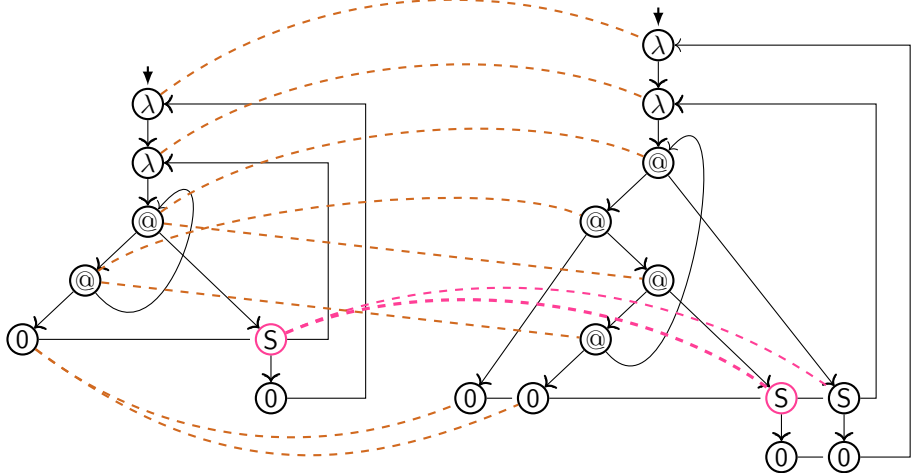
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$\llbracket L_0 \rrbracket_{\mathcal{T}}$

$\llbracket L \rrbracket_{\mathcal{T}}$

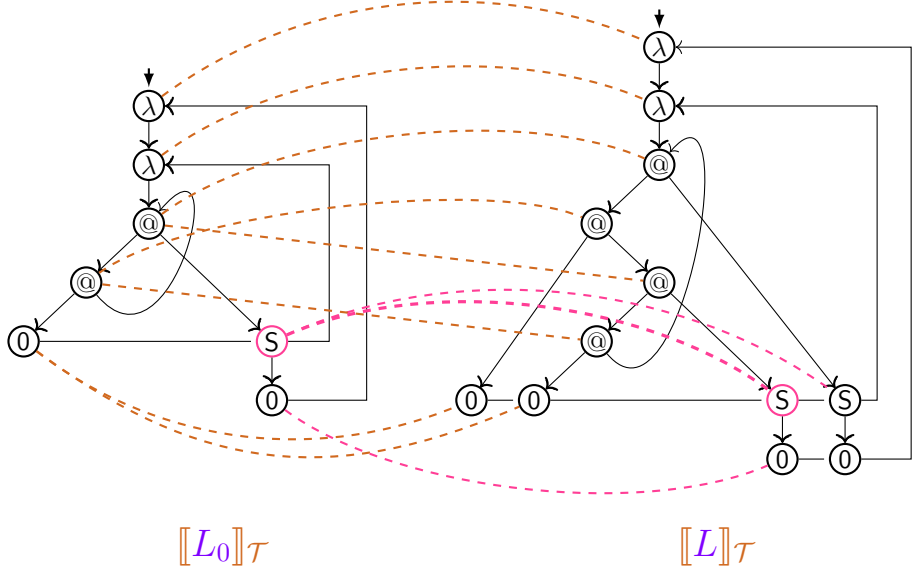
# bisimulation check between $\lambda$ -term-graphs



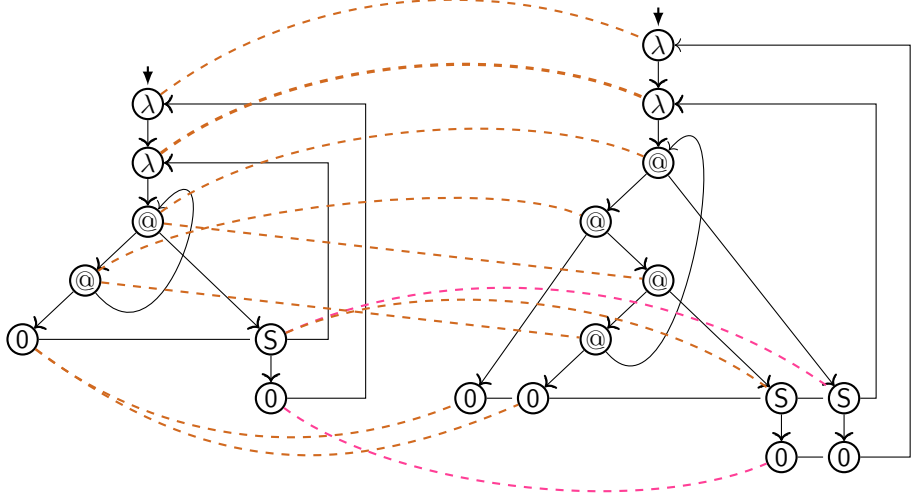
$\llbracket L_0 \rrbracket_{\mathcal{T}}$

$\llbracket L \rrbracket_{\mathcal{T}}$

# bisimulation check between $\lambda$ -term-graphs



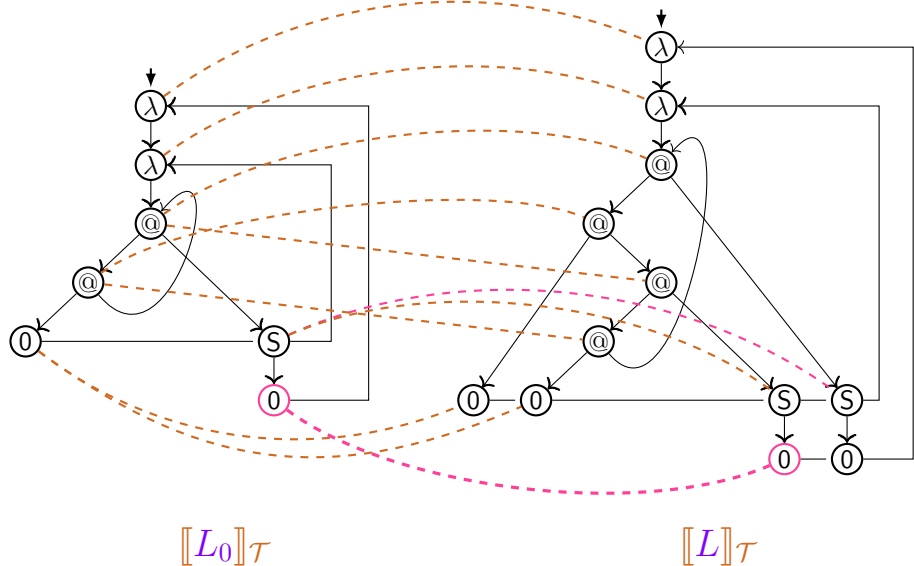
# bisimulation check between $\lambda$ -term-graphs



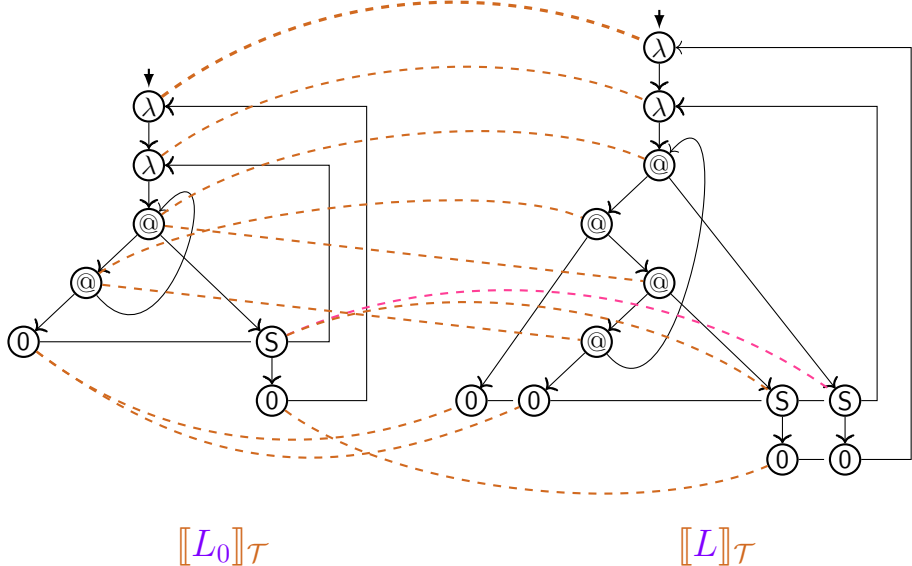
$\llbracket L_0 \rrbracket_{\mathcal{T}}$

$\llbracket L \rrbracket_{\mathcal{T}}$

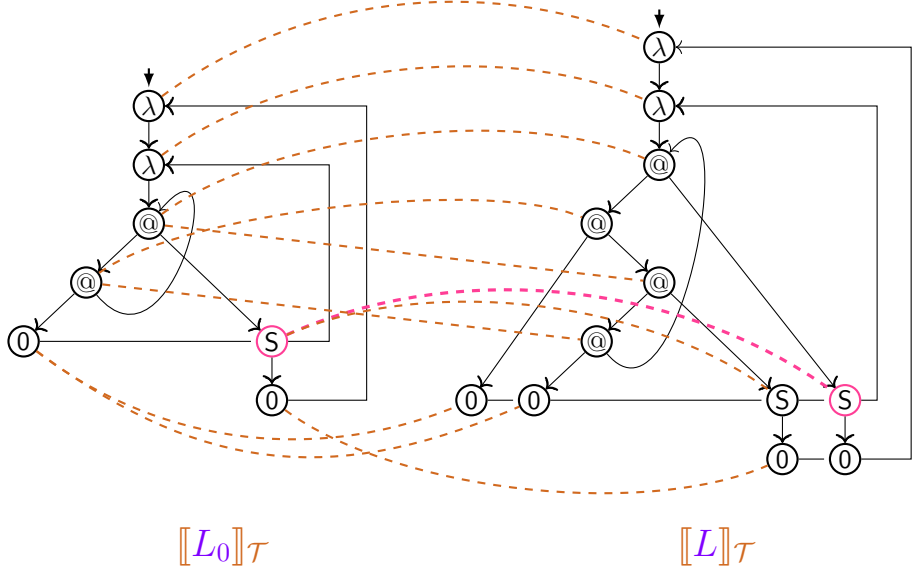
# bisimulation check between $\lambda$ -term-graphs



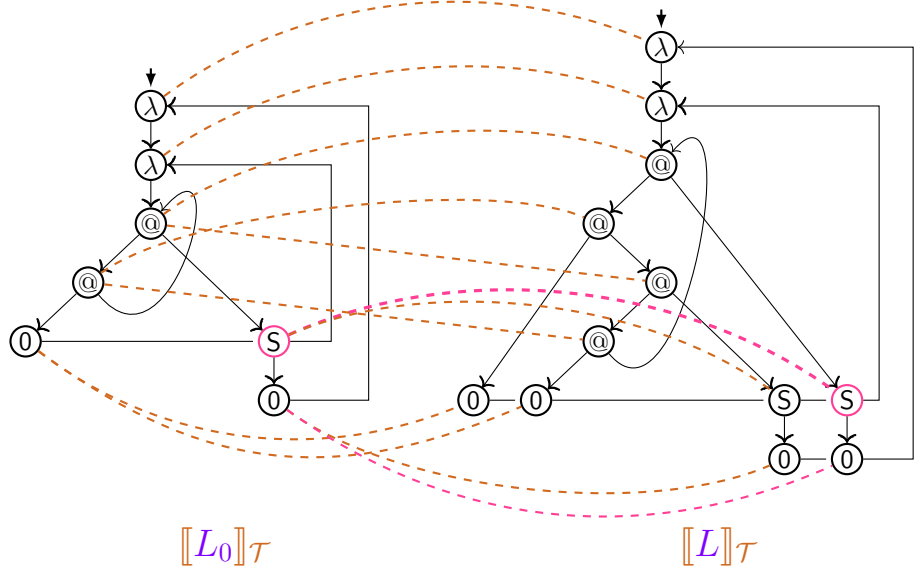
# bisimulation check between $\lambda$ -term-graphs



# bisimulation check between $\lambda$ -term-graphs

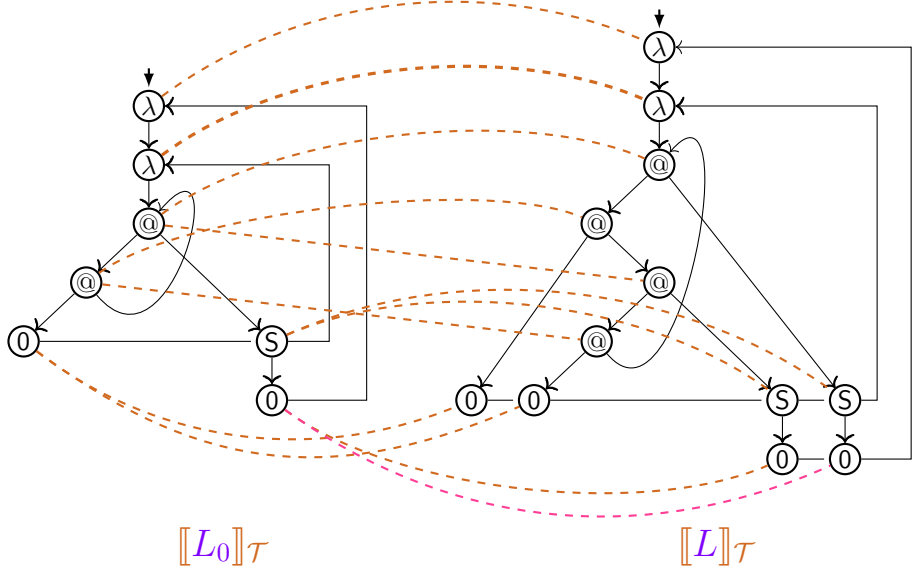


# bisimulation check between $\lambda$ -term-graphs

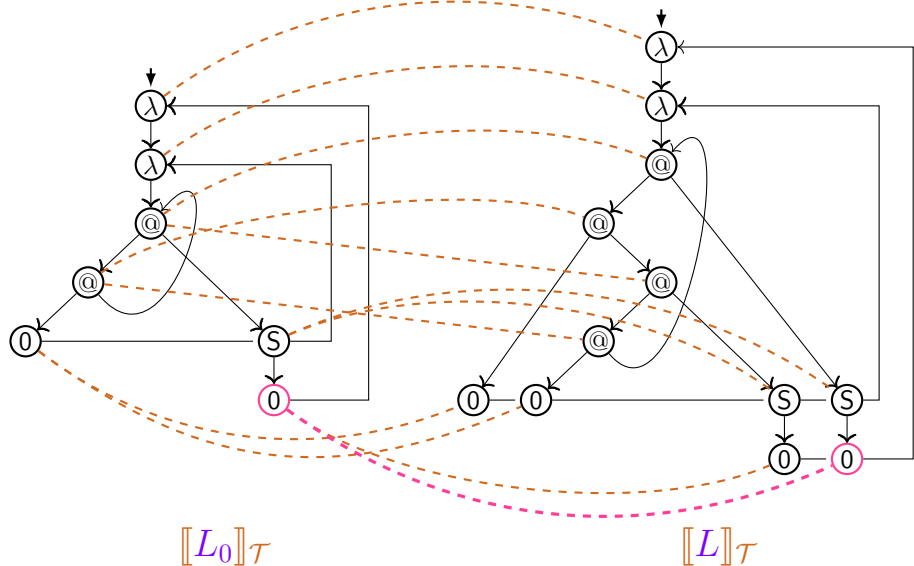




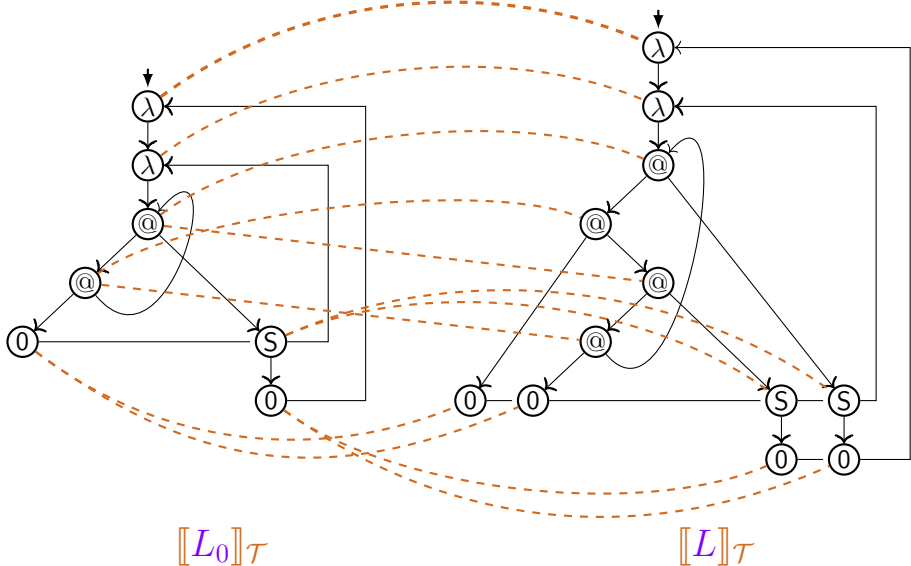
# bisimulation check between $\lambda$ -term-graphs



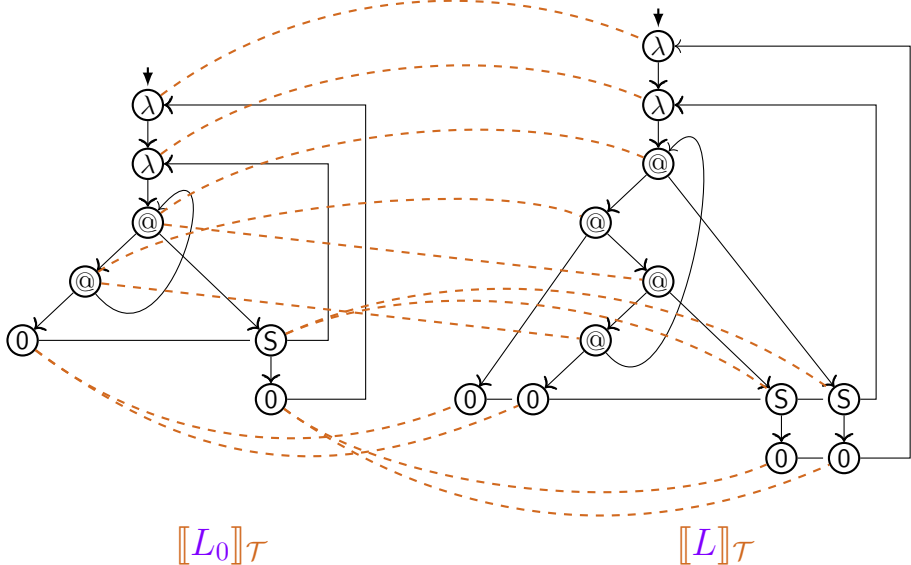
# bisimulation check between $\lambda$ -term-graphs



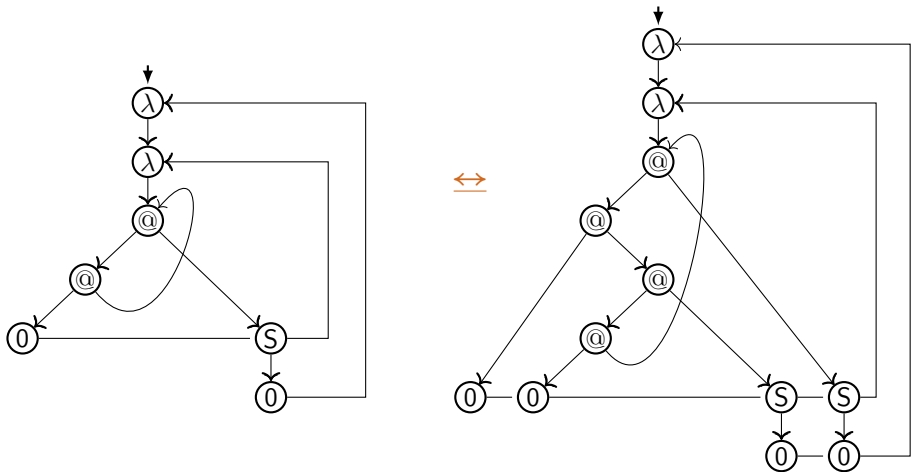
# bisimulation check between $\lambda$ -term-graphs



# bisimulation between $\lambda$ -term-graphs



# bisimilarity between $\lambda$ -term-graphs

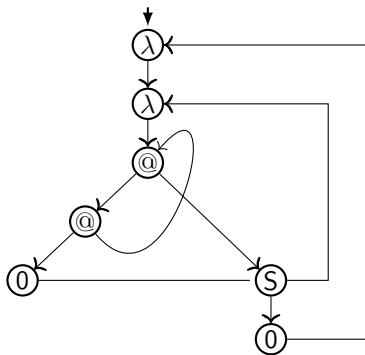


$\llbracket L_0 \rrbracket_{\mathcal{T}}$

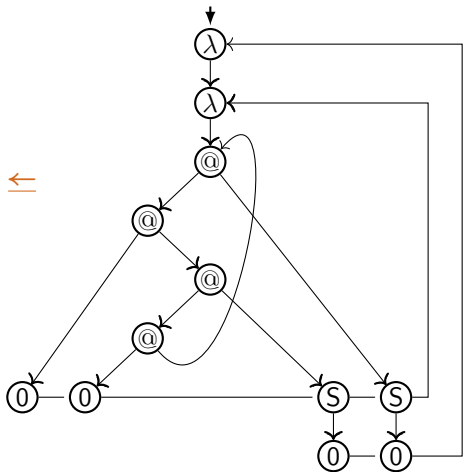
$\Leftrightarrow$

$\llbracket L \rrbracket_{\mathcal{T}}$

# functional bisimilarity and bisimulation collapse



$\llbracket L_0 \rrbracket_{\mathcal{T}}$



$\Leftarrow$

$\llbracket L \rrbracket_{\mathcal{T}}$

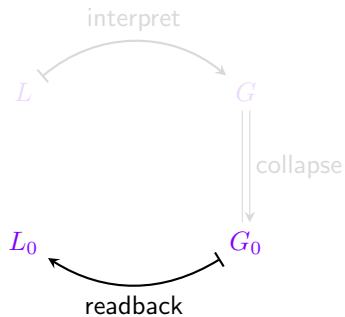
# bisimulation collapse: property

## Theorem

The class of *eager-scope  $\lambda$ -term-graphs*  
is closed under *functional bisimilarity*  $\Rightarrow$ .

$\Rightarrow$  For a  $\lambda_{\text{letrec}}$ -term  $L$   
the *bisimulation collapse* of  $\llbracket L \rrbracket_{\mathcal{T}}$  is again an *eager-scope  $\lambda$ -term-graph*.

# readback





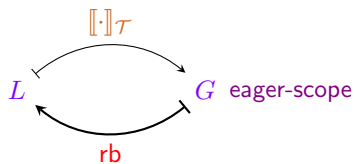
# readback

defined with property:



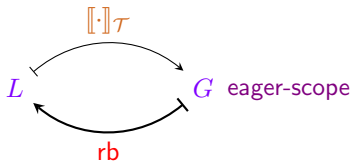
# readback

defined with property:



# readback

defined with property:



## Theorem

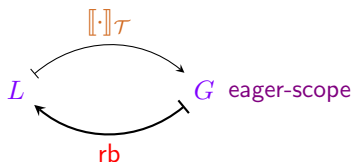
For all *eager-scope*  $\lambda$ -term-graphs  $G$ :

$$([[ \cdot ] ]_{\mathcal{T}} \circ rb)(G) \simeq G$$

The readback  $rb$  is a right-inverse of  $[[\cdot]]_{\mathcal{T}}$  modulo isomorphism  $\simeq$ .

# readback

defined with property:



## Theorem

For all *eager-scope*  $\lambda$ -term-graphs  $G$ :

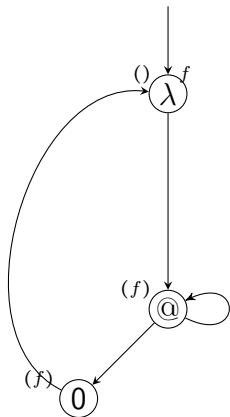
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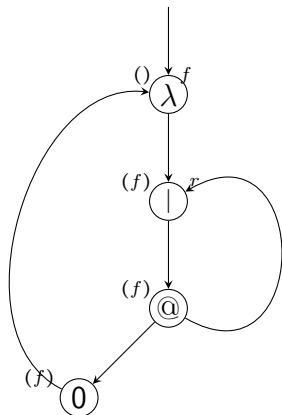
idea:

1. construct a spanning tree  $T$  of  $G$
2. using local rules, in a bottom-up traversal of  $T$  synthesize  $L = rb(G)$

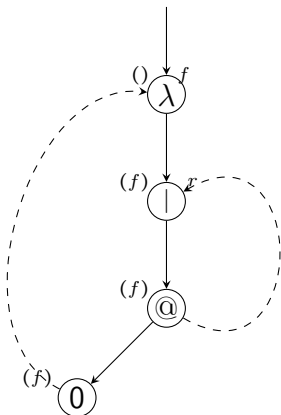
# readback: example (fix)



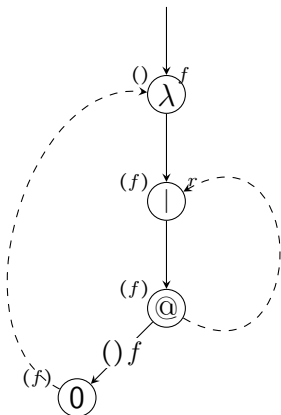
# readback: example (fix)



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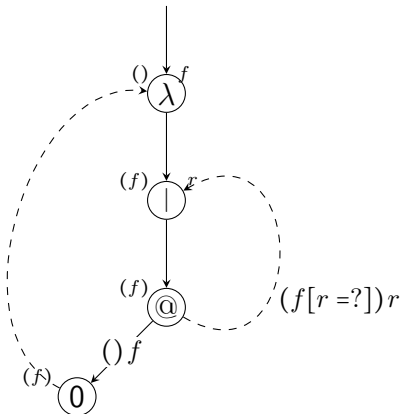


# readback: example (fix)

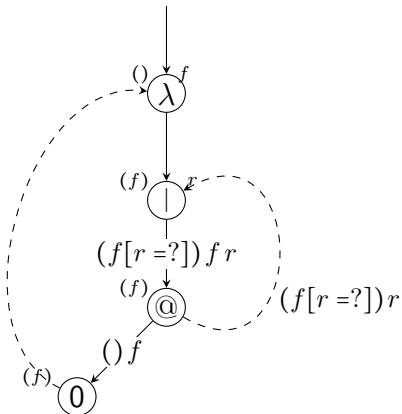




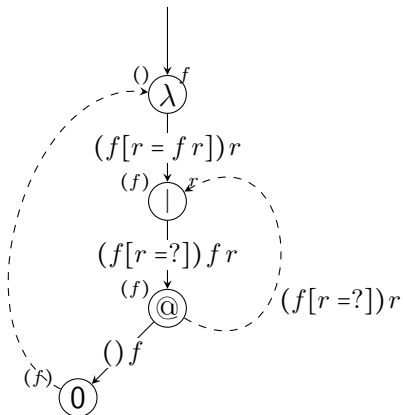
# readback: example (fix)



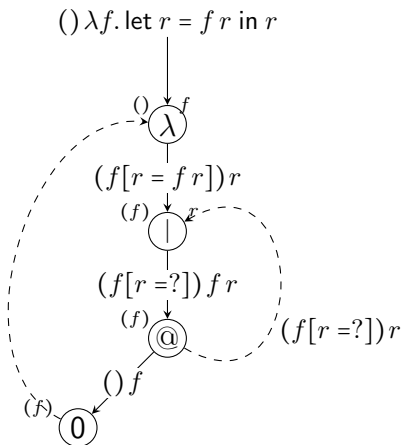
# readback: example (fix)



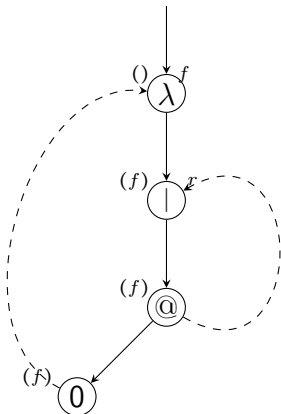
# readback: example (fix)



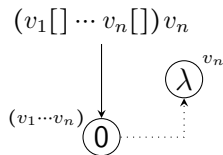
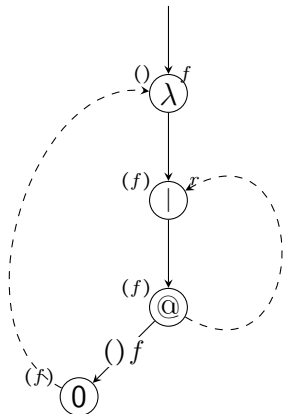
# readback: example (fix)



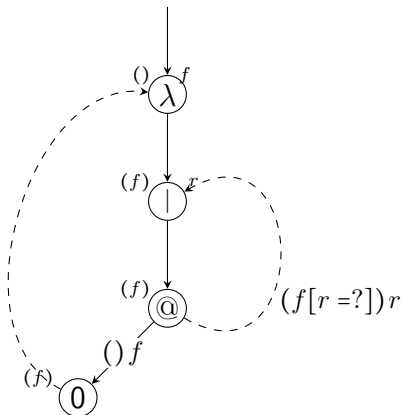
# readback: example (fix)



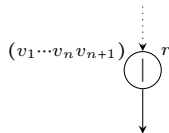
# readback: example (fix)



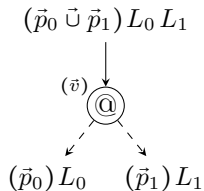
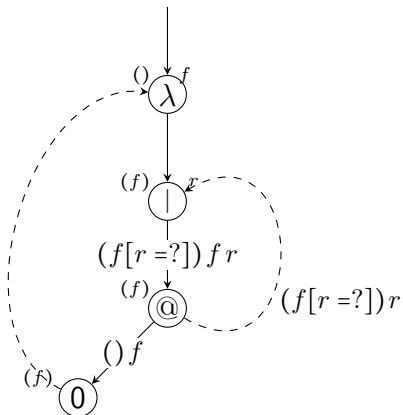
# readback: example (fix)



$$(v_1[] \cdots v_n[] v_{n+1}[r = ?])r$$

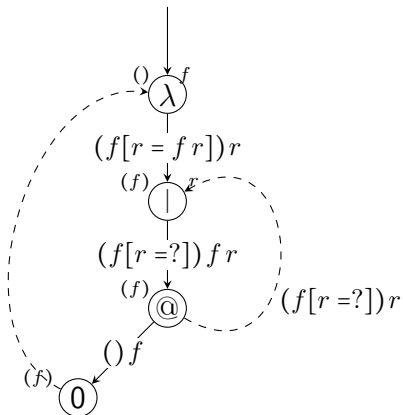


# readback: example (fix)



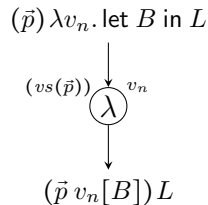
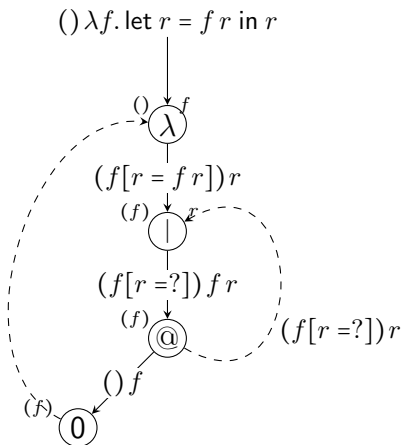


# readback: example (fix)

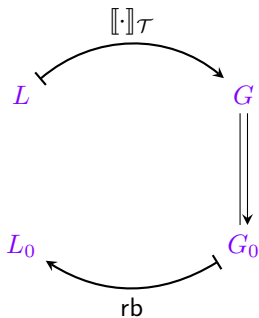


$$\begin{array}{c}
 (\vec{p} v_{n+1}[B, r = L]) r \\
 \downarrow \\
 (vs(\vec{p}) v_{n+1}) \downarrow r \\
 \downarrow \\
 (\vec{p} v_{n+1}[B, (r = ?)]) L
 \end{array}$$

# readback: example (fix)



# maximal sharing: complexity



## 1. interpretation

of  $\lambda_{\text{letrec}}$ -term  $L$  with  $|L| = n$

as  $\lambda$ -term-graph  $G = \llbracket L \rrbracket_{\mathcal{T}}$

▶ in time  $O(n^2)$ , size  $|G| \in O(n^2)$ .

## 2. bisimulation collapse $\Downarrow$

of f-o term graph  $G$  into  $G_0$

▶ in time  $O(|G| \log |G|) = O(n^2 \log n)$

## 3. readback rb

of f-o term graph  $G_0$

yielding  $\lambda_{\text{letrec}}$ -term  $L_0 = \text{rb}(G_0)$ .

▶ in time  $O(|G| \log |G|) = O(n^2 \log n)$

## Theorem

Computing a maximally compact form  $L_0 = (\text{rb} \circ \Downarrow \circ \llbracket \cdot \rrbracket_{\mathcal{T}})(L)$  of  $L$  for a  $\lambda_{\text{letrec}}$ -term  $L$  requires time  $O(n^2 \log n)$ , where  $|L| = n$ .

# Demo: console output

```
jan:~/papers/maxsharing-ICFP/talks/ICFP-2014> maxsharing running.l
```

```
λ-letrec-term:
```

```
λx. λf. let r = f (f r x) x in r
```

derivation:

```

----- 0
(x f[r]) f      (x f[r]) r      (x) x
----- @
(x f[r]) f r      (x f[r]) x      S
----- 0
(x f[r]) f      (x f[r]) f r x      @
----- 0
(x f[r]) f      (x f[r]) f r x      (x) x
----- @
(x f[r]) f (f r x)      (x f[r]) x      S
----- @
(x f[r]) f (f r x) x      (x f[r]) r      let
----- λ
(x f) let r = f (f r x) x in r
----- λ
(x) λf. let r = f (f r x) x in r
----- λ
() λx. λf. let r = f (f r x) x in r

```

```
writing DFA to file: running-dfa.pdf
```

```
readback of DFA:
```

```
λx. λy. let F = y (y F x) x in F
```

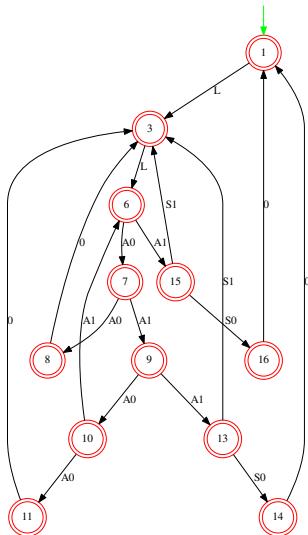
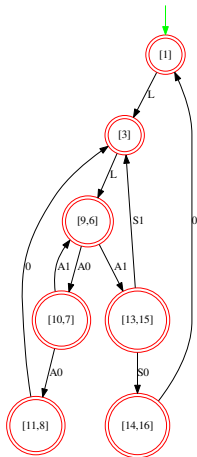
```
writing minimised DFA to file: running-mindfa.pdf
```

```
readback of minimised DFA:
```

```
λx. λy. let F = y F x in F
```

```
jan:~/papers/maxsharing-ICFP/talks/ICFP-2014> █
```

# Demo: generated $\lambda$ -NFAs

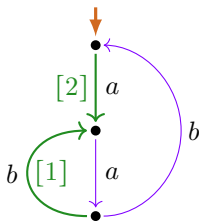


# Resources (maximal sharing)

- ▶ tool [maxsharing](#) on [hackage.haskell.org](#)
- ▶ papers and reports
  - ▶ [Maximal Sharing in the Lambda Calculus with Letrec](#)
    - ▶ ICFP 2014 paper
    - ▶ accompanying report [arXiv:1401.1460](#)
  - ▶ [Term Graph Representations for Cyclic Lambda Terms](#)
    - ▶ TERMGRAPH 2013 proceedings
    - ▶ extended report [arXiv:1308.1034](#)
  - ▶ Vincent van Oostrom, CG: [Nested Term Graphs](#)
    - ▶ TERMGRAPH 2014 post-proceedings in [EPTCS 183](#)
- ▶ thesis Jan Rochel
  - ▶ [Unfolding Semantics of the Untyped  \$\lambda\$ -Calculus with letrec](#)
    - ▶ [Ph.D. Thesis](#), Utrecht University, 2016

# Process interpretation of regular expressions

(current work with Wan Fokkink)



# Regular Expressions

## Definition

The set  $\text{Reg}(A)$  of **regular expressions** over alphabet  $A$  is defined by the grammar:

$$e, f ::= 0 \mid 1 \mid a \mid (e + f) \mid (e \cdot f) \mid (e^*) \quad (\text{for } a \in A).$$



# Regular Expressions

## Definition

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$$e, f ::= 0 \mid 1 \mid a \mid (e + f) \mid (e \cdot f) \mid (e^*) \quad (\text{for } a \in A).$$

Note, here:

- ▶ symbol  $0$  instead of  $\emptyset$
- ▶ symbol  $1$  used (often dropped, definable as  $0^*$ )
- ▶ **no** complementation operation  $\bar{e}$ 
  - ▶ **is not expressible** under language interpretation

# Language interpretation $\llbracket \cdot \rrbracket_{\mathcal{L}}$ (S.C. Kleene, 1951)

$0 \xrightarrow{\llbracket \cdot \rrbracket_{\mathcal{L}}} \text{empty language } \emptyset$

$1 \xrightarrow{\llbracket \cdot \rrbracket_{\mathcal{L}}} \{\epsilon\} \quad (\epsilon \text{ the empty word})$

$a \xrightarrow{\llbracket \cdot \rrbracket_{\mathcal{L}}} \{a\}$

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$a \xrightarrow{\llbracket \cdot \rrbracket_{\mathcal{L}}} \{a\}$

$e + f \xrightarrow{\llbracket \cdot \rrbracket_{\mathcal{L}}} \text{union of } \llbracket e \rrbracket_{\mathcal{L}} \text{ and } \llbracket f \rrbracket_{\mathcal{L}}$

$e \cdot f \xrightarrow{\llbracket \cdot \rrbracket_{\mathcal{L}}} \text{element-wise concatenation of } \llbracket e \rrbracket_{\mathcal{L}} \text{ and } \llbracket f \rrbracket_{\mathcal{L}}$

$e^* \xrightarrow{\llbracket \cdot \rrbracket_{\mathcal{L}}} \text{set of words formed by concatenating words in } \llbracket e \rrbracket_{\mathcal{L}},$   
and adding the empty word  $\epsilon$

# Process interpretation $\llbracket \cdot \rrbracket^P$ (R. Milner, 1984)

- 0  $\xrightarrow{\llbracket \cdot \rrbracket^P}$  deadlock  $\delta$ , no termination
- 1  $\xrightarrow{\llbracket \cdot \rrbracket^P}$  empty process  $\epsilon$ , then terminate
- $a$   $\xrightarrow{\llbracket \cdot \rrbracket^P}$  atomic action  $a$ , then terminate

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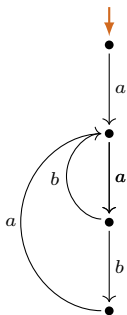
$a \xrightarrow{\llbracket \cdot \rrbracket_{\mathcal{P}}} \text{atomic action } a, \text{ then terminate}$

$e + f \xrightarrow{\llbracket \cdot \rrbracket_{\mathcal{P}}} \text{alternative composition of } \llbracket e \rrbracket_{\mathcal{P}} \text{ and } \llbracket f \rrbracket_{\mathcal{P}}$

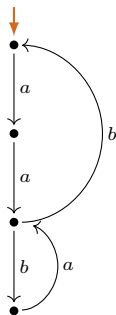
$e \cdot f \xrightarrow{\llbracket \cdot \rrbracket_{\mathcal{P}}} \text{sequential composition of } \llbracket e \rrbracket_{\mathcal{P}} \text{ and } \llbracket f \rrbracket_{\mathcal{P}}$

$e^* \xrightarrow{\llbracket \cdot \rrbracket_{\mathcal{P}}} \text{unbounded iteration of } \llbracket e \rrbracket_{\mathcal{P}}, \text{ option to terminate}$

# Process interpretation of regular expressions

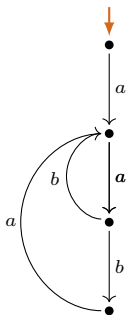


$$a(a(b + ba))^*0$$

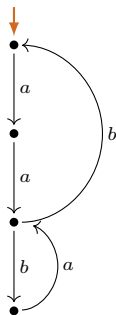


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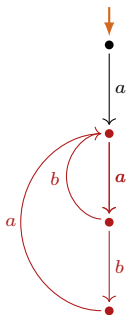


$$a \cdot (a \cdot (b + b \cdot a))^* \cdot 0$$

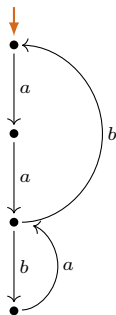


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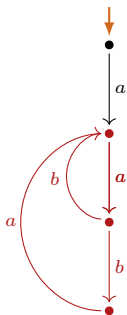
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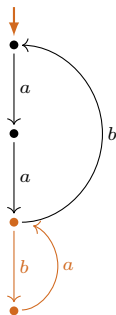
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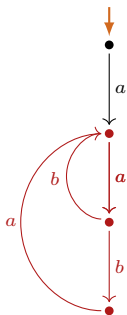


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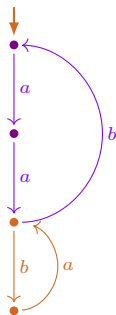


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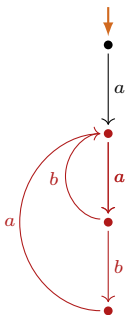


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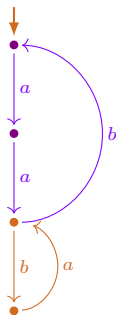


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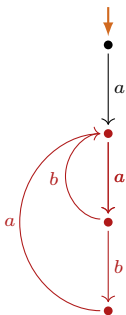


$$\llbracket a \cdot (a \cdot (b + b \cdot a))^* \cdot 0 \rrbracket_P$$

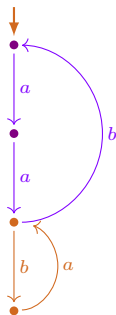


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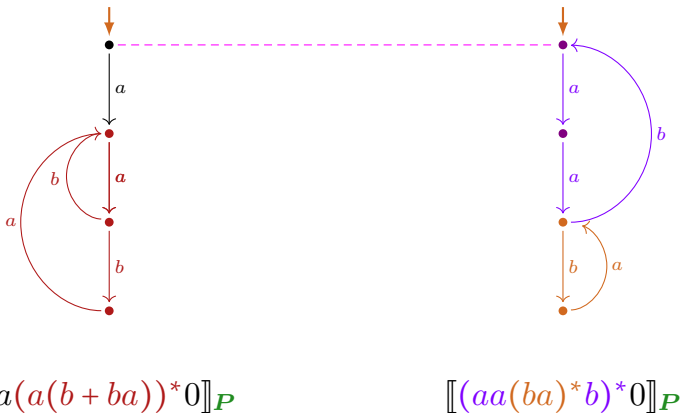


$$\llbracket a(a(b+ba))^*0 \rrbracket_{\mathcal{P}}$$

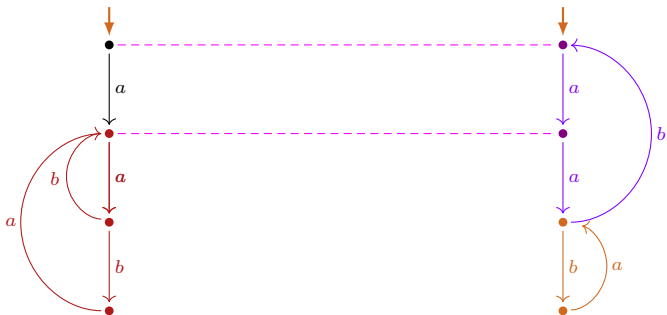


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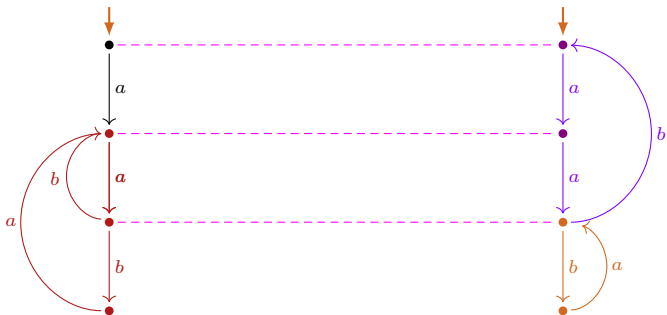
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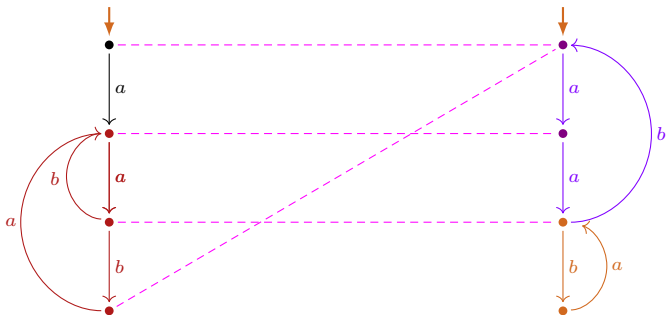
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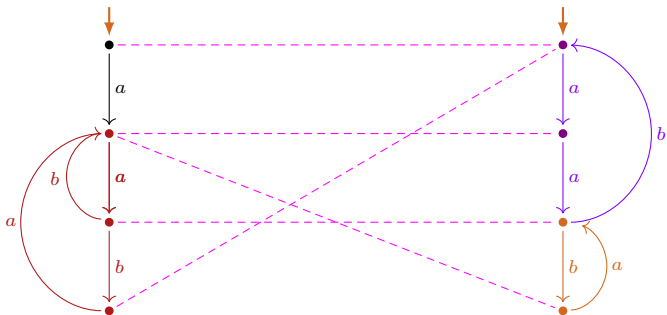


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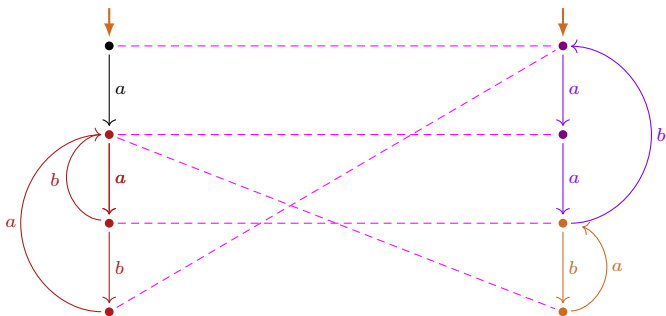
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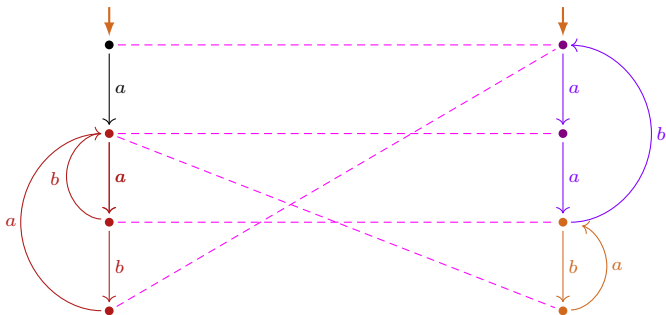
$$\llbracket ((aa(ba))^*b)^*0 \rrbracket_{\mathcal{P}}$$

# Process interpretation of regular expressions



$$\llbracket a(a(b+ba))^*0 \rrbracket_{\mathcal{P}} \quad \Leftrightarrow \quad \llbracket ((aa(ba))^*b)^*0 \rrbracket_{\mathcal{P}}$$

# Process interpretation of regular expressions



$$a(a(b + ba))^*0$$

$$\Leftrightarrow_P$$

$$(aa(ba)^*b)^*0$$

# Process graphs and NFAs

## Definition

A **process graph** over actions in  $A$  is a tuple  $G = \langle V, v_s, T, E \rangle$  where:

- ▶  $V$  is a set of *vertices*,
- ▶  $v_s \in V$  is the *start vertex*,
- ▶  $T \subseteq V \times A \times V$  the set of *transitions*,
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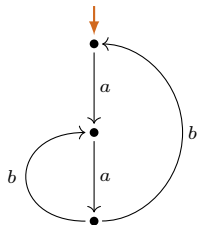
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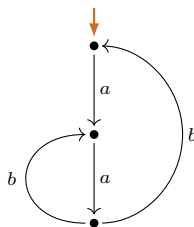
*Antimirov (1996)*: **NFA-definition of  $\llbracket \cdot \rrbracket_{\mathcal{P}}$  via partial derivatives.**

# Expressible process graphs (under bisimulation $\leftrightarrow$ )



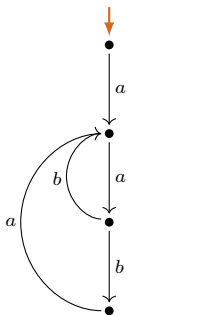


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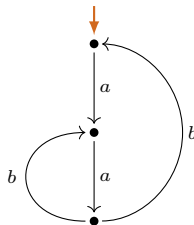
$$\notin \text{im}(\llbracket \cdot \rrbracket_P)$$

# Expressible process graphs (under bisimulation $\leftrightarrow$ )



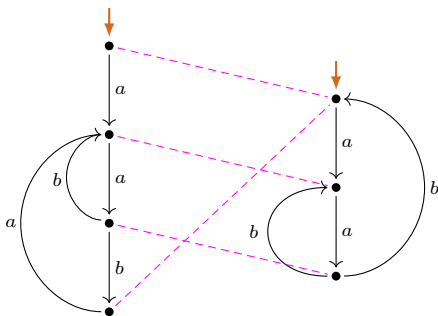
$\in im([\cdot]_{\mathcal{P}})$

$[\cdot]_{\mathcal{P}}$ -expressible



$\notin im([\cdot]_{\mathcal{P}})$

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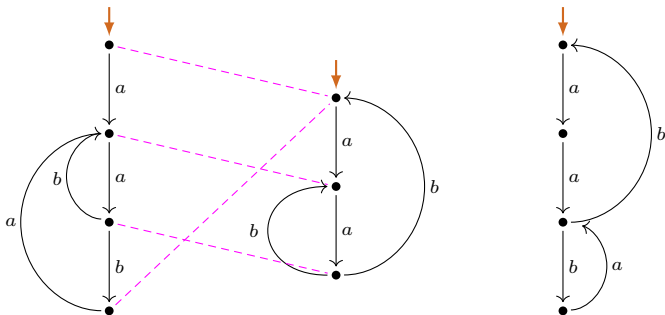


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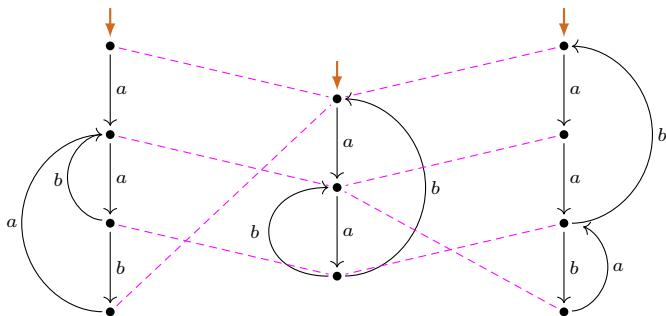
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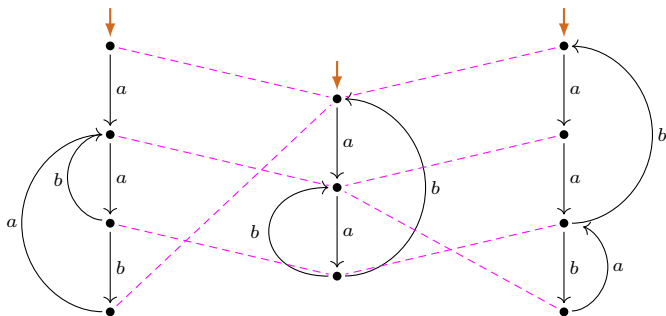
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# Expressible process graphs (under bisimulation $\Leftrightarrow$ )



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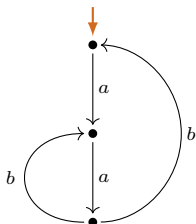
$[\cdot]_{\mathcal{P}}$ -expressible  
modulo  $\Leftrightarrow$

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# Properties of $P$

- ▶ **Not** every finite-state process is  $\llbracket \cdot \rrbracket_P$ -expressible.

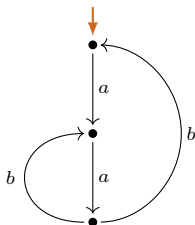


**not**  $\llbracket \cdot \rrbracket_P$ -expressible

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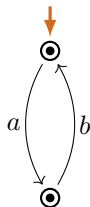
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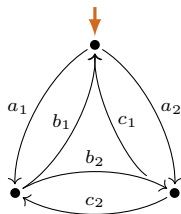
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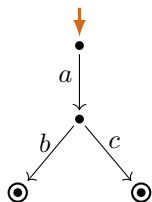


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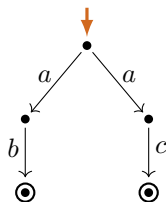
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$$a \cdot (b + c)$$

$=_L$

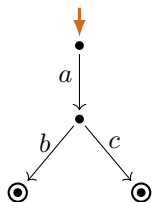


$$a \cdot b + a \cdot c$$

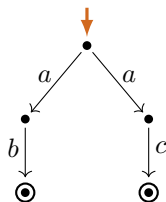
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$$a \cdot (b + c)$$



$$a \cdot b + a \cdot c$$



# Salomaa's axiomatization of $=_L$ (products commuted)

*Axioms:*

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$$(B5) \quad e \cdot (f + g) = e \cdot f + e \cdot g$$

$$(B6) \quad e + e = e$$

$$(B7) \quad e \cdot 1 = e$$

$$(B8) \quad e \cdot 0 = 0$$

$$(B9) \quad e + 0 = e$$

$$(B10) \quad e^* = 1 + e \cdot e^*$$

$$(B11) \quad e^* = (1 + e)^*$$

*Inference rules:* equational logic *plus*

$$\frac{e = f \cdot e + g}{e = f^* \cdot g} \text{ FIX } \underbrace{(\text{if } \{\epsilon\} \notin \llbracket f \rrbracket_L)}_{\text{non-empty-word property}}$$

# Sound and **unsound** axioms with respect to $\leftrightarrow_P$

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# Milner's adaptation for $\Leftrightarrow_P$ (Mil = Mil<sup>-</sup> + RSP<sup>\*</sup>)

*Axioms:*

$$(B1) \quad e + (f + g) = (e + f) + g$$

$$(B2) \quad (e \cdot f) \cdot g = e \cdot (f \cdot g)$$

$$(B3) \quad e + f = f + e$$

$$(B4) \quad (e + f) \cdot g = e \cdot g + f \cdot g$$

$$(B6) \quad e + e = e$$

$$(B7) \quad e \cdot 1 = e$$

$$(B8)' \quad 0 \cdot e = 0$$

$$(B9) \quad e + 0 = e$$

$$(B10) \quad e^* = 1 + e \cdot e^*$$

$$(B11) \quad e^* = (1 + e)^*$$

*Inference rules:* equational logic plus

$$\frac{e = f \cdot e + g}{e = f^* \cdot g} \text{RSP}^* \left( \underbrace{\text{(if } \{\epsilon\} \notin \llbracket f \rrbracket_L)}_{\text{non-empty-word property}} \right)$$

# Milner's questions

Q2. Is Mil complete for  $\Leftrightarrow_P$  ?

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Q1. Which structural property of finite process graphs characterizes  $\llbracket \cdot \rrbracket_P$ -expressibility modulo  $\Leftrightarrow$ ?

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- ▶ definability by well-behaved specifications (*Baeten/Corradini, 2005*)

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# Milner's questions, and partial results

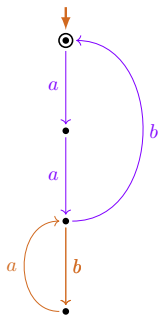
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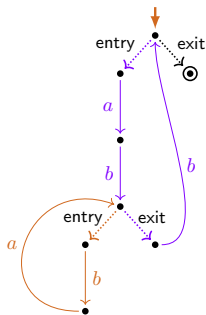
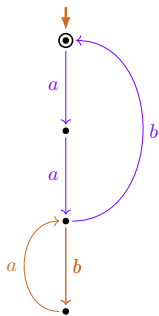
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- ▶  $\text{Mil}^-$  + one of two stronger rules (than  $\text{RSP}^*$ ) is complete (G, 2006)
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  - ▶ with a unique solvability principle USP

# Well-behaved form, looping palm trees



$$\llbracket (aa(ba)^*b)^* \rrbracket_P$$

# Well-behaved form, looping palm trees

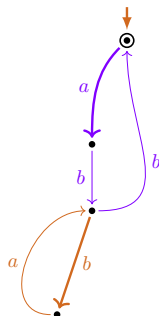
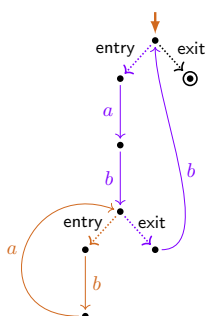
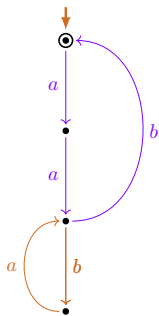


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# Loop chart

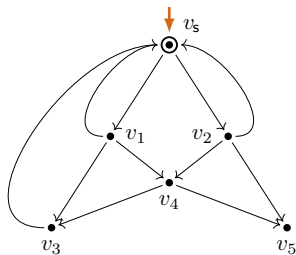
## Definition

A process graph is a **loop chart** if:

L-1.

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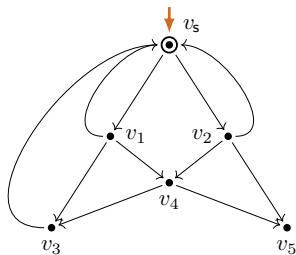


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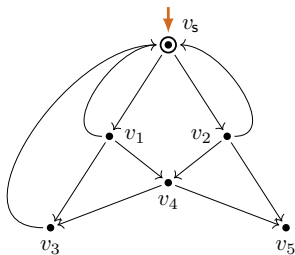


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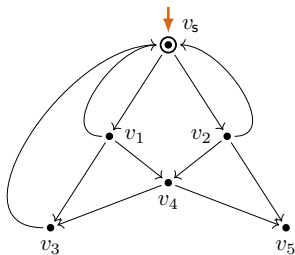


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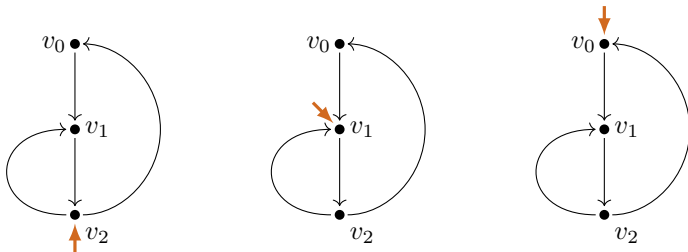


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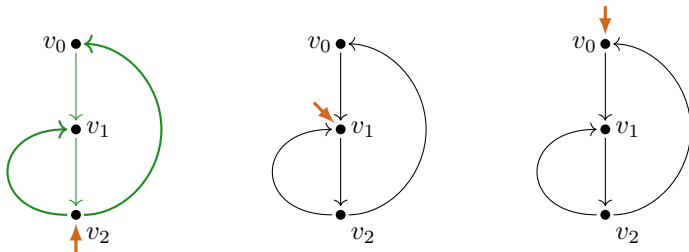


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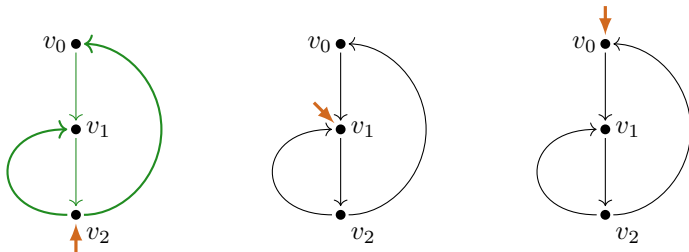


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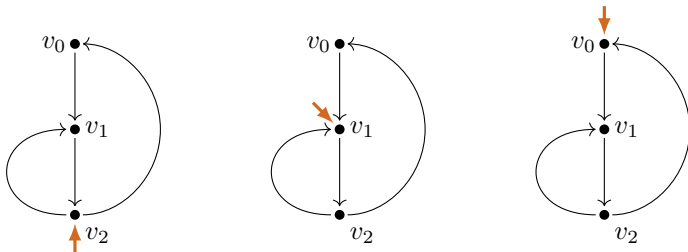
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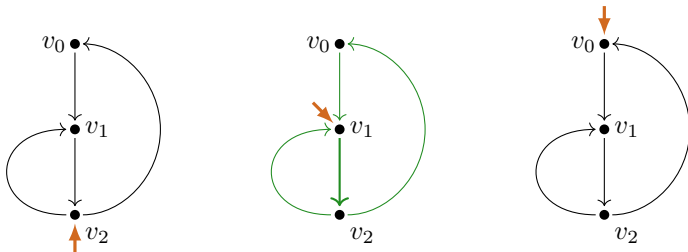
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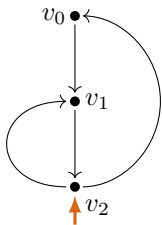
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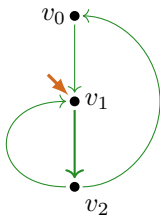
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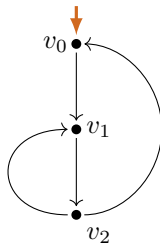
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loop chart

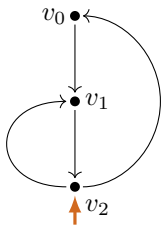


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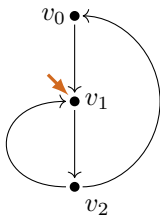
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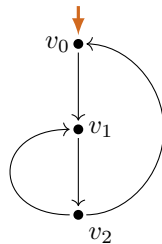
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loop chart



loop chart



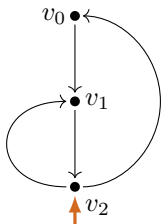


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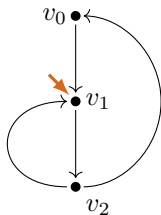
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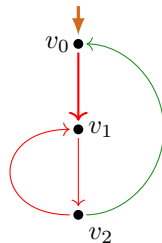
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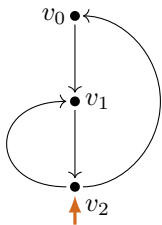


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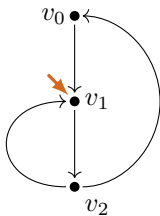
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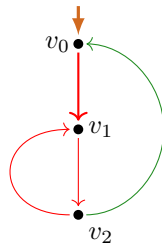
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loop chart



loop chart



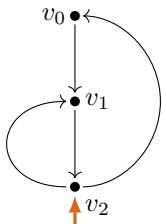
no loop chart

# Loop chart

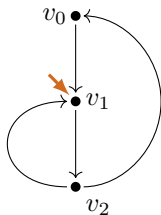
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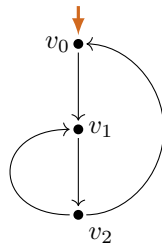
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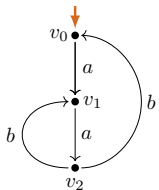


loop chart

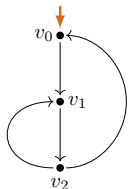


no loop chart

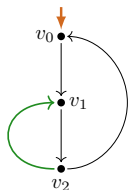
# Loop elimination



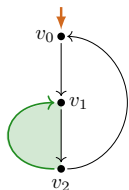
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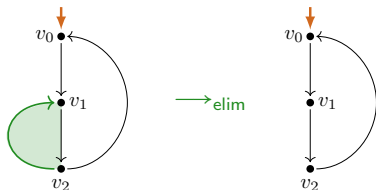
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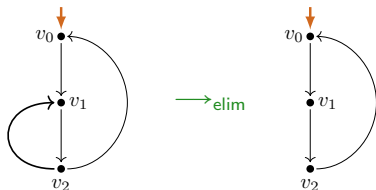


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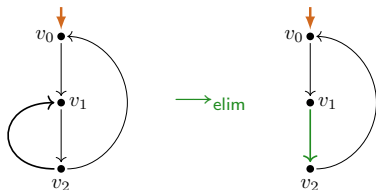




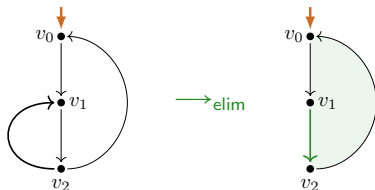
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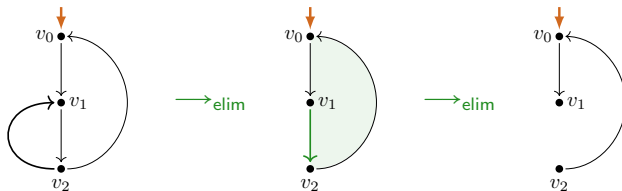
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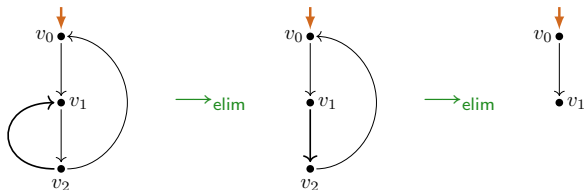
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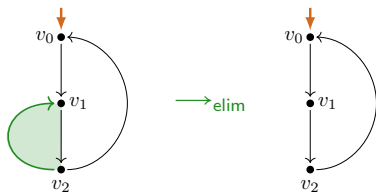
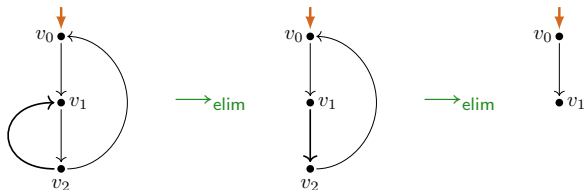
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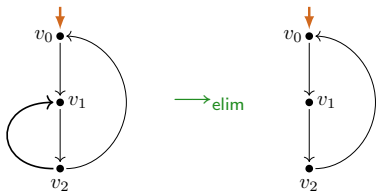
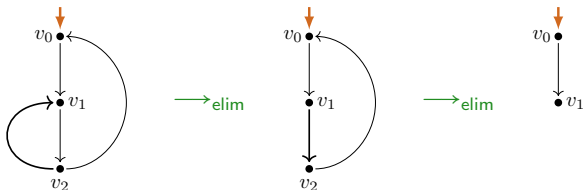
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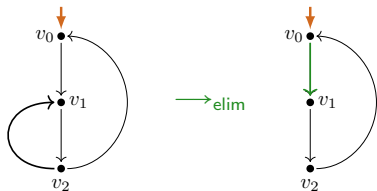
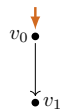
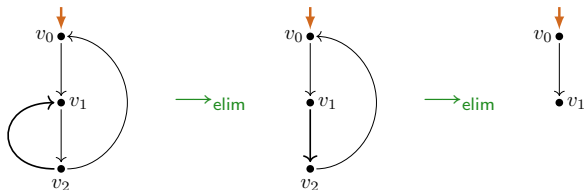
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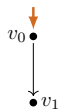
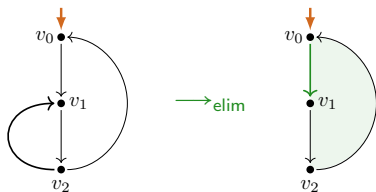
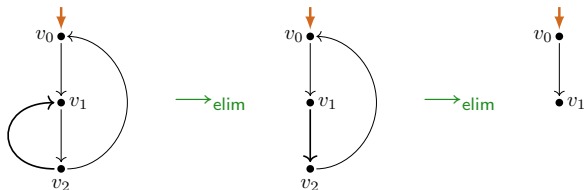


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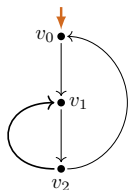




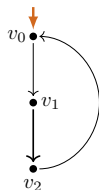
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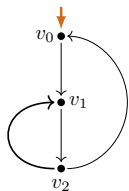
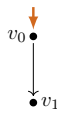
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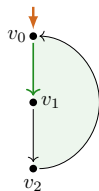
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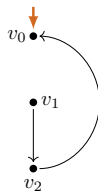
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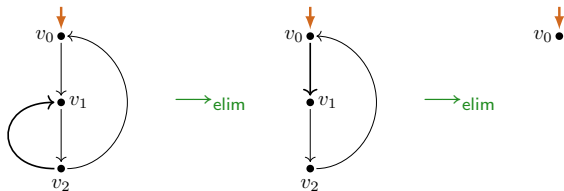
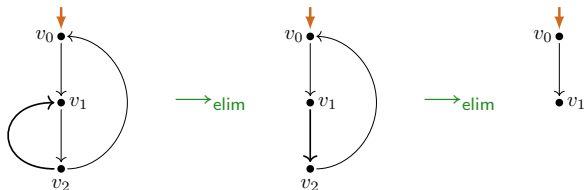
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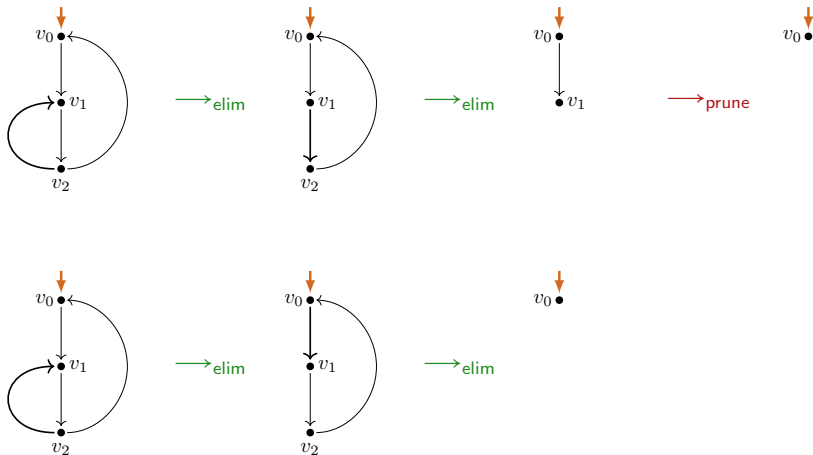
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# Loop elimination



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# Loop elimination, and properties

- <sub>elim</sub> : eliminate a transition-induced loop by:
  - ▶ removing the loop-entry transition(s)
  - ▶ garbage collection
- <sub>prune</sub> : remove a transition to a deadlocking state

# Loop elimination, and properties

$\longrightarrow_{\text{elim}}$  : eliminate a transition-induced loop by:

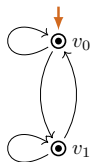
- ▶ removing the loop-entry transition(s)
- ▶ garbage collection

$\longrightarrow_{\text{prune}}$  : remove a transition to a deadlocking state

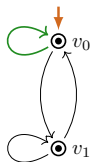
## Lemma

- (i)  $\longrightarrow_{\text{elim}}$  *is terminating.*
- (ii)  $\longrightarrow_{\text{elim}} \cup \longrightarrow_{\text{prune}}$  *is terminating and confluent.*

# Loop elimination

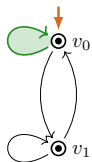


# Loop elimination





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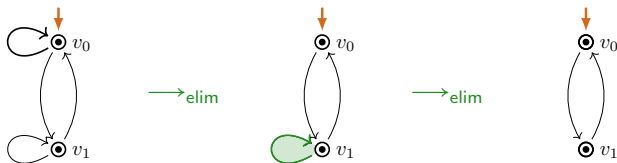
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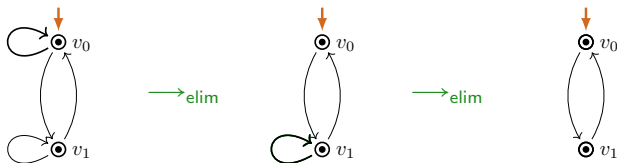
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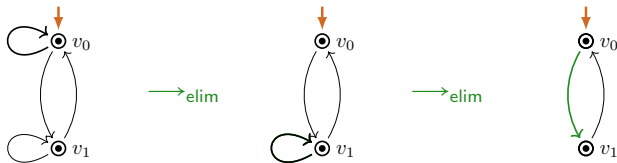
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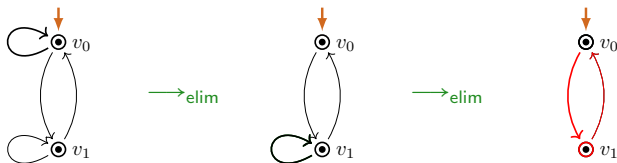


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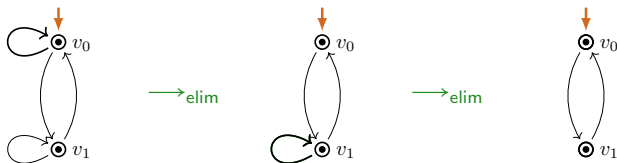




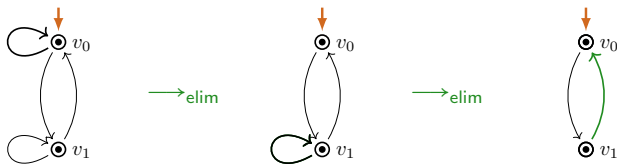
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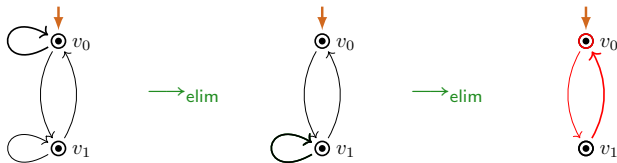
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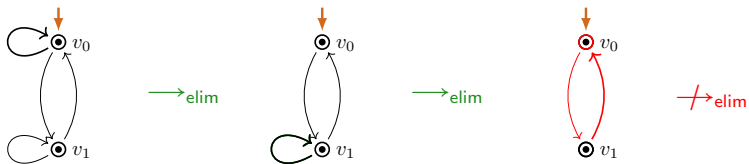
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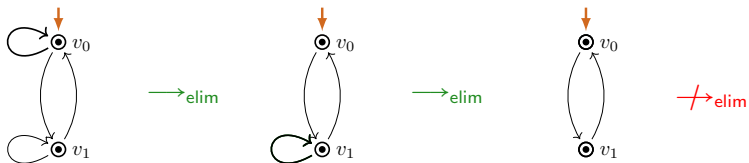
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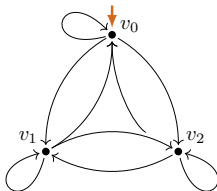
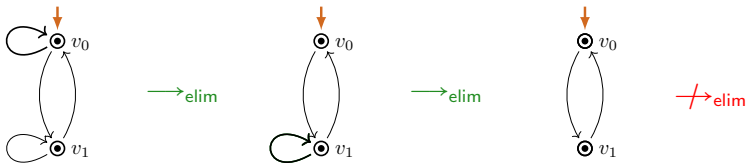
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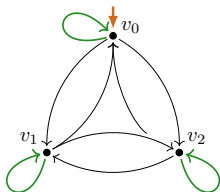
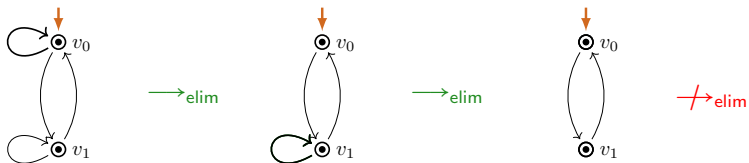
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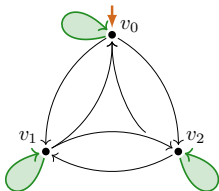
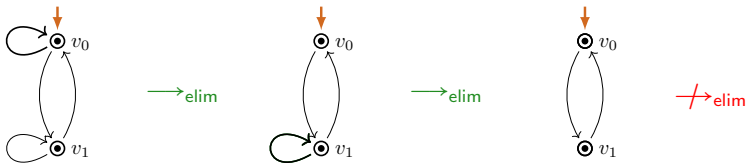


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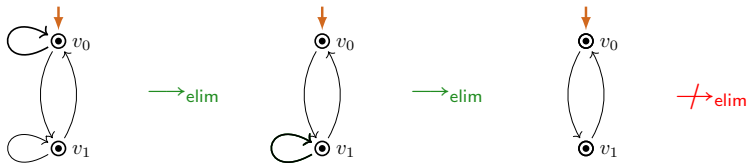




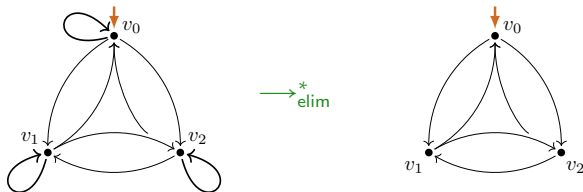
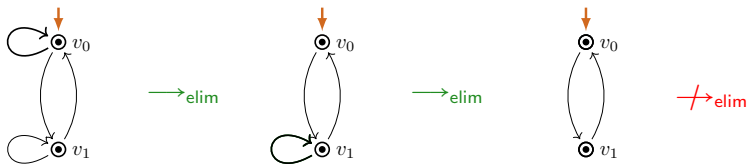
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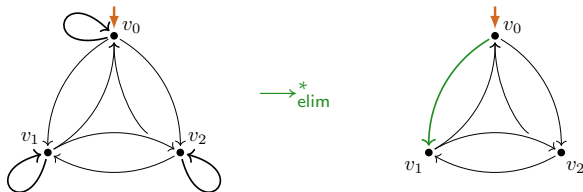
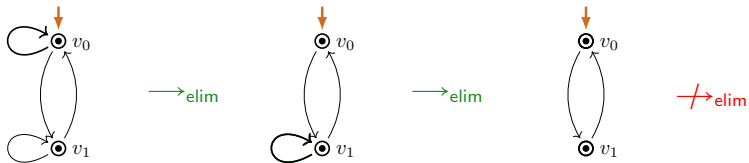
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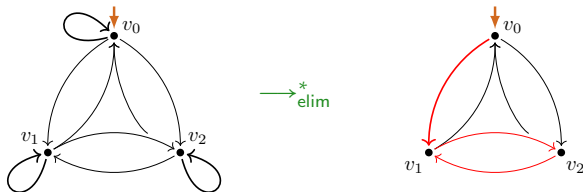
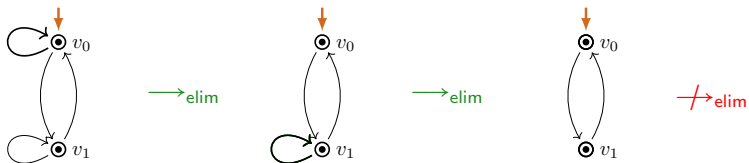
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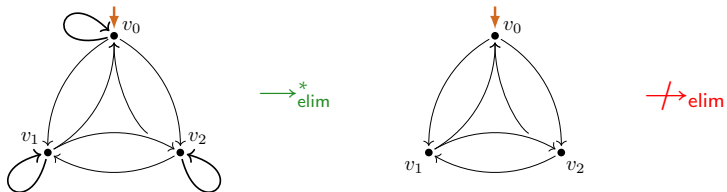
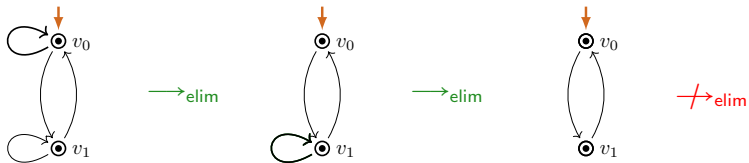
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# Structure property LEE

## Definition

A process graph  $G$  satisfies **LEE** (*loop existence and elimination*) if:

$$\exists G_0 \left( G \xrightarrow{*}_{\text{elim}} G_0 \not\rightarrow_{\text{elim}} \right. \\ \left. \wedge G_0 \text{ has no infinite trace} \right).$$

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## Lemma (by using confluence properties)

For every process graph  $G$  the following are equivalent:

- (i)  $\text{LEE}(G)$ .
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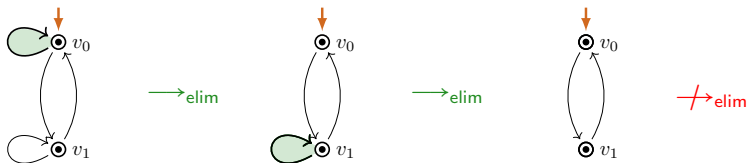
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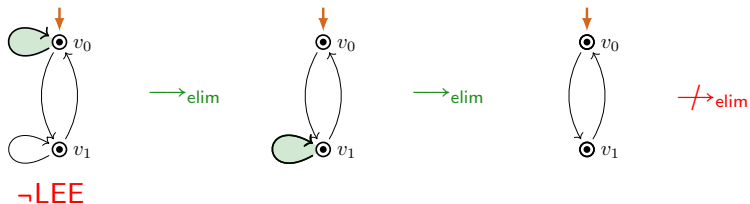
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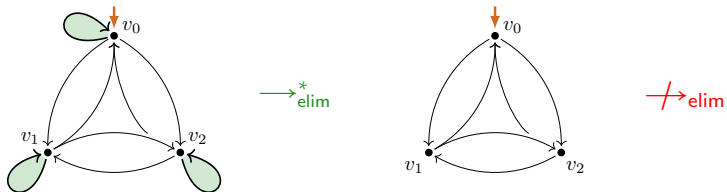
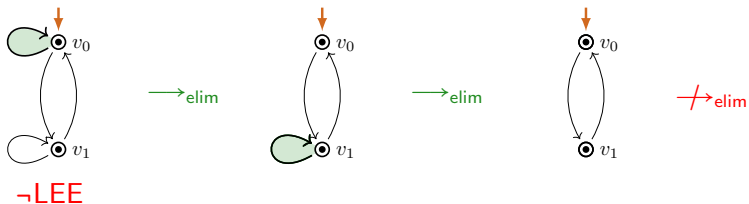
# LEE fails



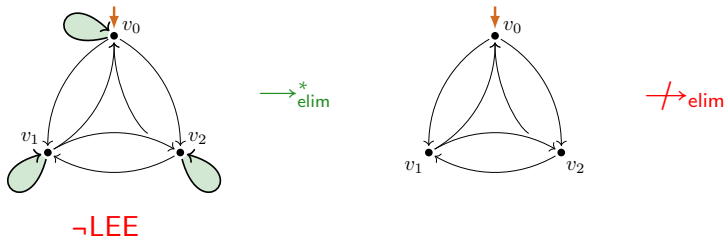
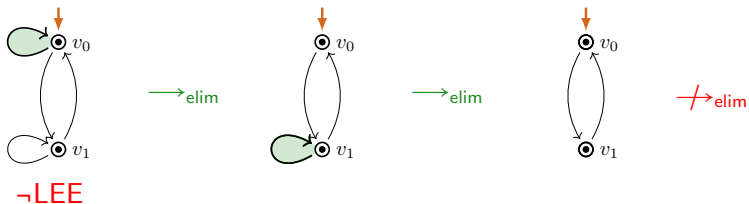
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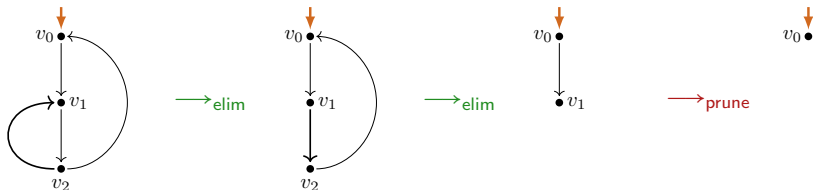
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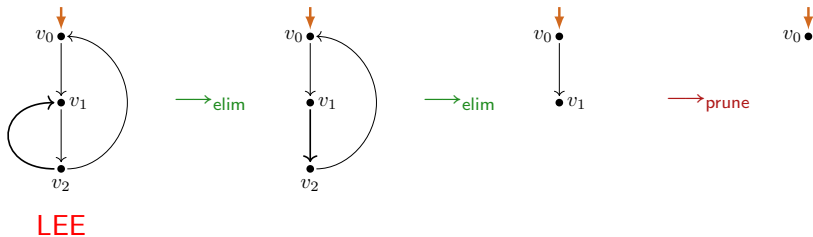
# LEE fails



# LEE holds

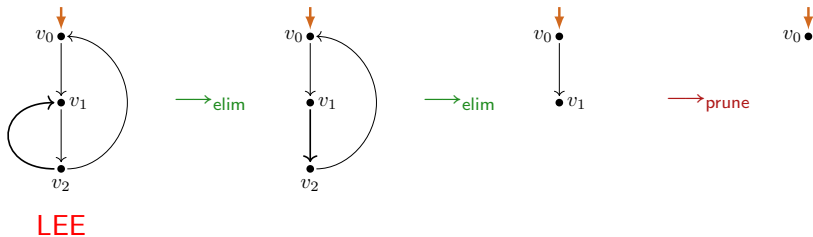


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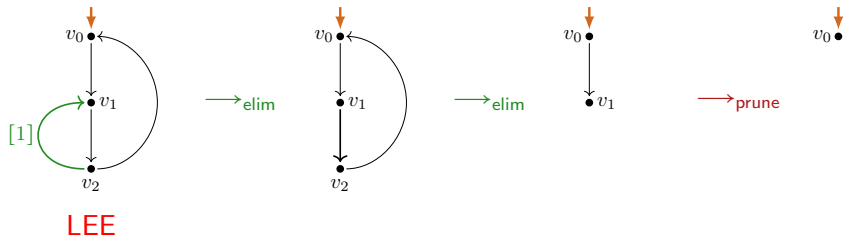




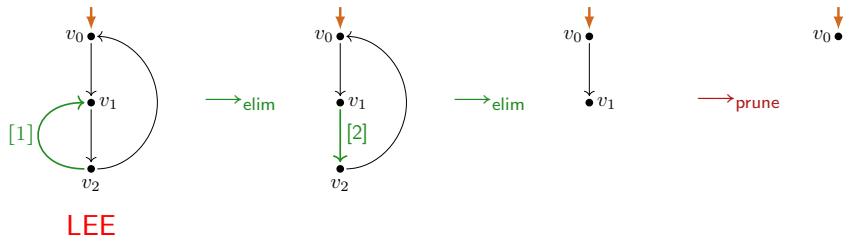
# LEE holds / Recording loop elimination



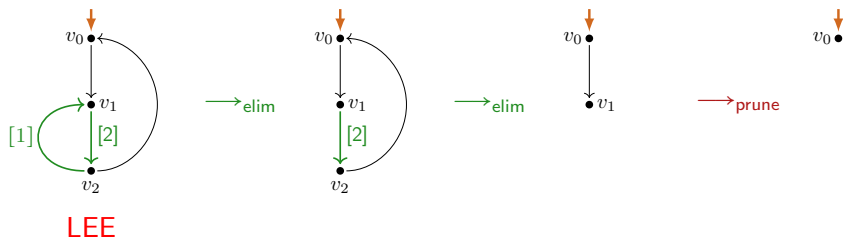
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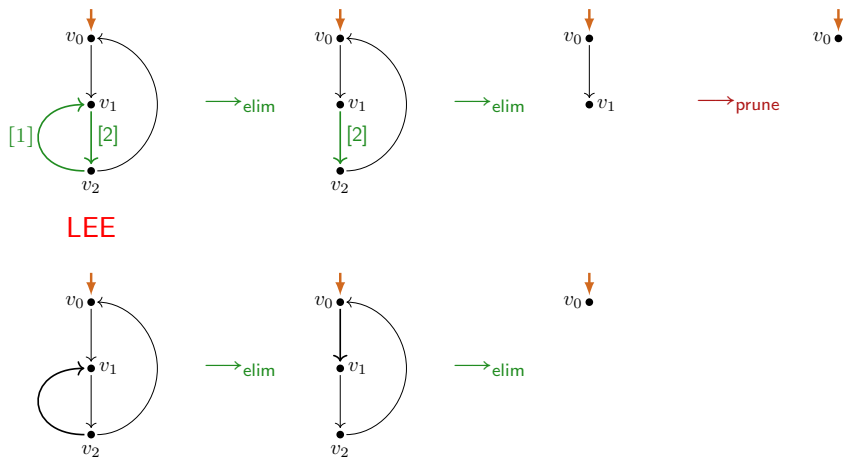
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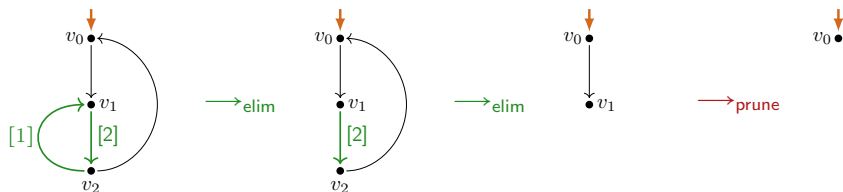
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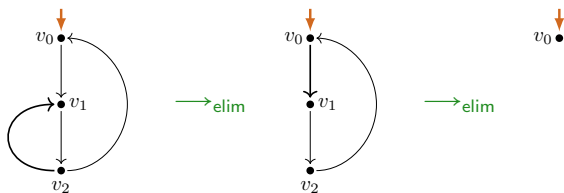
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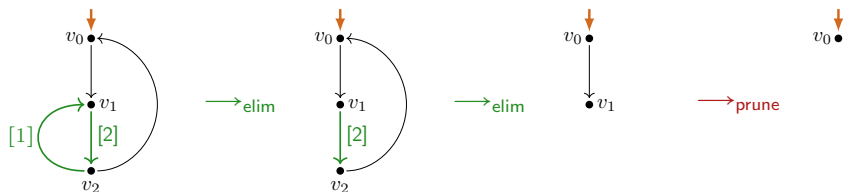


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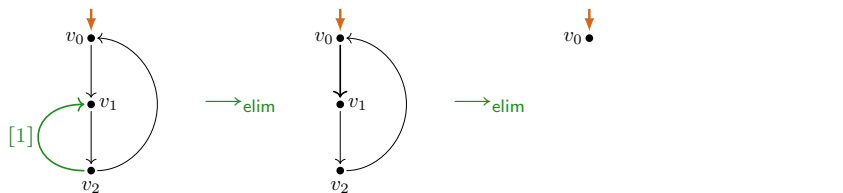


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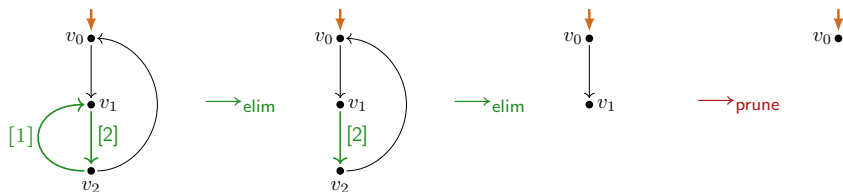


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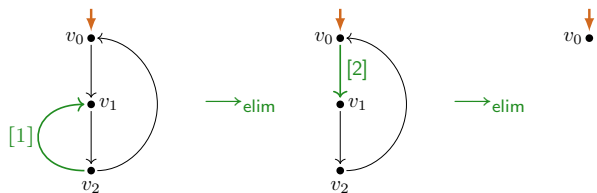


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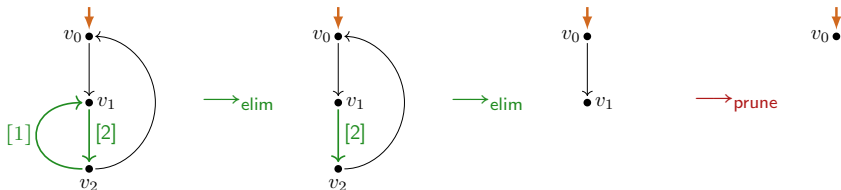
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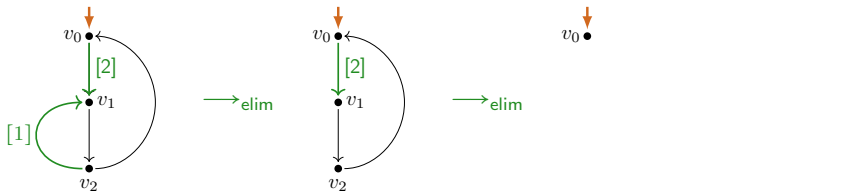
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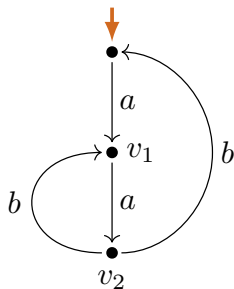


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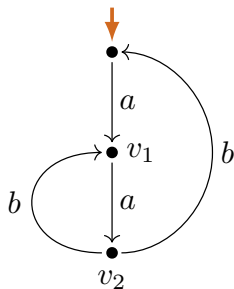
LEE

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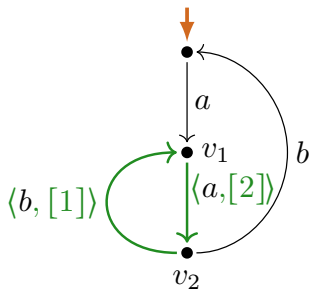
loop-branch labeling: marking transitions  $\xrightarrow{a}$  as:



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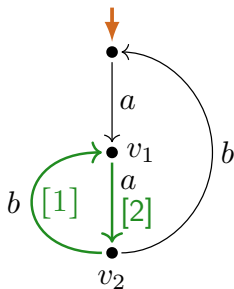
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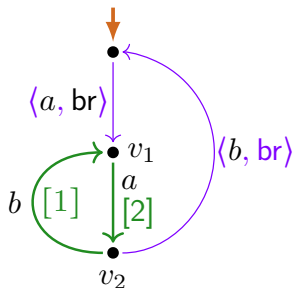
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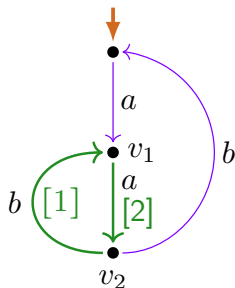
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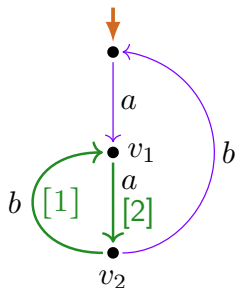
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A loop-branch labeling is a **LEE-witness**, if:

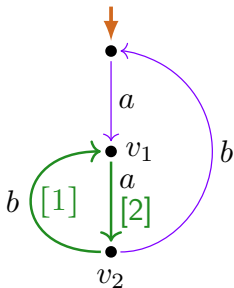
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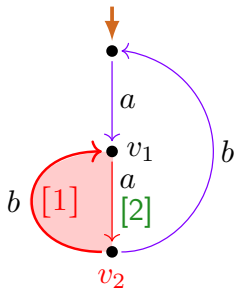
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$$\mathcal{L}(v_2, \xrightarrow{[1]}, \xrightarrow{br, [ > 1 ]})$$

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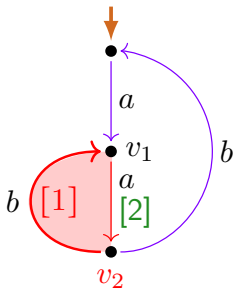
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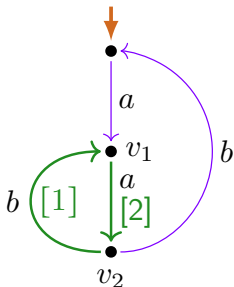
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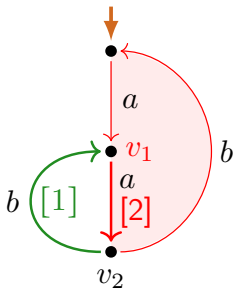
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$$\mathcal{L}(v_1, \xrightarrow{a}_{[2]}, \xrightarrow{a}_{br, [>2]})$$

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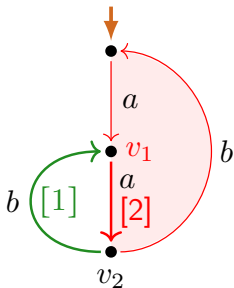
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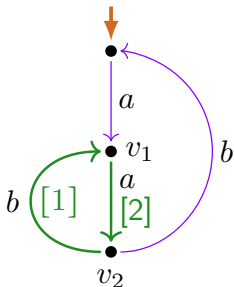
## Definition

A loop-branch labeling is a **LEE-witness**, if:

- L1.  $\forall n \in \mathbb{N} \forall v \in V \left( v \xrightarrow{a}_{[n]} \Rightarrow \mathcal{L}(v, \xrightarrow{a}_{[n]}, \xrightarrow{a}_{br, [>n]}) \text{ is a loop subchart} \right)$ .
- L2.
- L3.

$\mathcal{L}(v, \xrightarrow{a}_{[n]}, \xrightarrow{a}_{br, [>n]}) :=$  subchart induced  
by entry steps  $\xrightarrow{a}_{[n]}$  from  $v$   
followed by branch steps  $\xrightarrow{a}_{br}$   
or entry steps  $\xrightarrow{a}_{[m]}$  with  $m > n$ ,  
until  $v$  is reached again

# LEE-witness



loop-branch labeling: marking transitions  $\xrightarrow{a}$  as:

- ▶ entry steps  $\xrightarrow{\langle a, [n] \rangle}$  for  $n \in \mathbb{N}$ , written  $\xrightarrow{a}_{[n]}$ ,
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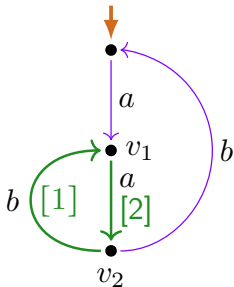
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## Definition

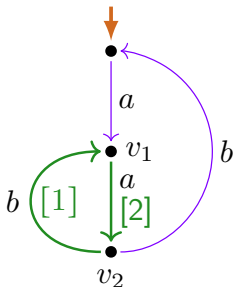
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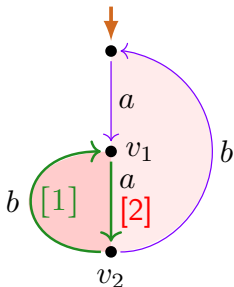
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$$\mathcal{L}(v_2, \rightarrow_{[1]}, \rightarrow_{br, [>1]})$$

$$\mathcal{L}(v_1, \rightarrow_{[2]}, \rightarrow_{br, [>2]})$$

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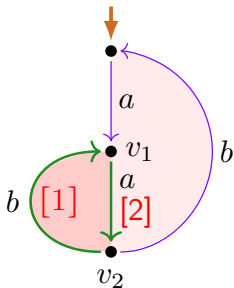
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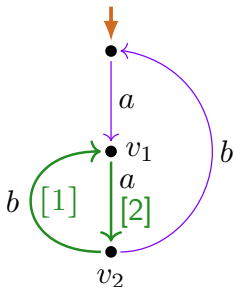
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LEE-witness

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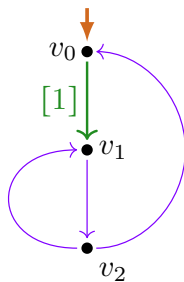
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A loop-branch labeling is a LEE-witness, if:

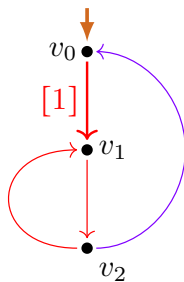
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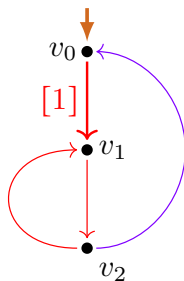
# LEE-witness ?



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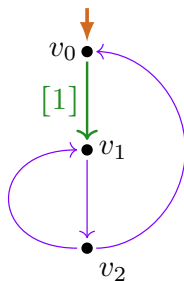
no!

(L1.) violated:

$\mathcal{L}(v_0, \rightarrow[1], \rightarrow_{br, [ > 1 ]})$

not a loop chart

# LEE-witness ?



no!

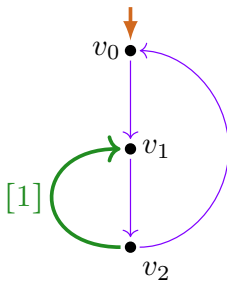
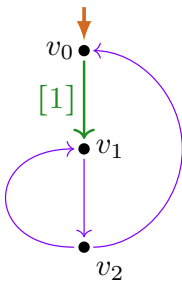
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not a loop chart



# LEE-witness ?



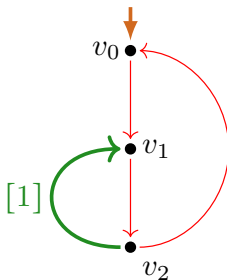
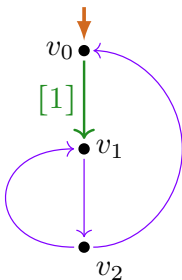
no!

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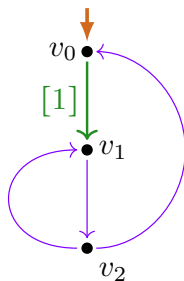
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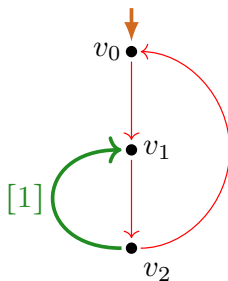


no!

(L1.) violated:

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not a loop chart



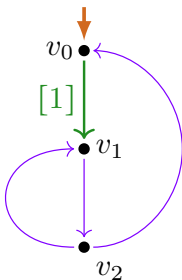
no!

(L2.) violated:

infinite  $\rightarrow_{br}$  path

from start vertex

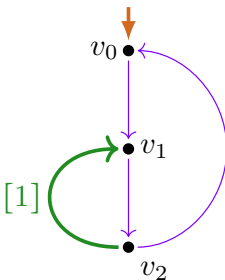
# LEE-witness ?



no!

(L1.) violated:

$\mathcal{L}(v_0, \rightarrow_{[1]}, \rightarrow_{br, [ > 1 ]})$   
not a loop chart

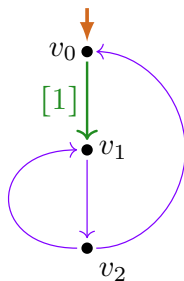


no!

(L2.) violated:

infinite  $\rightarrow_{br}$  path  
from start vertex

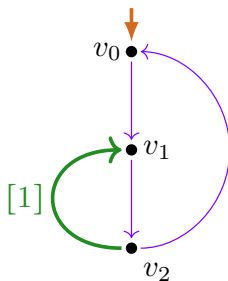
# LEE-witness ?



no!

(L1.) violated:

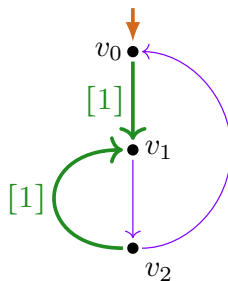
$\mathcal{L}(v_0, \rightarrow_{[1]}, \rightarrow_{br, [ > 1 ]})$   
not a loop chart



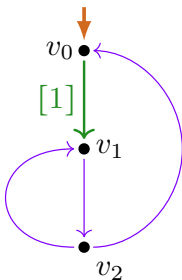
no!

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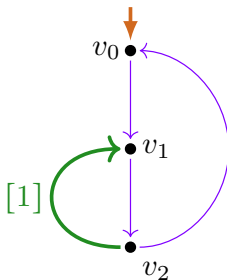
# LEE-witness ?



no!

(L1.) violated:

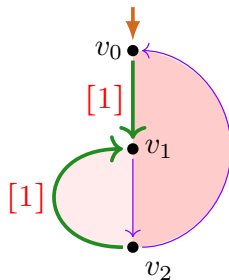
$\mathcal{L}(v_0, \rightarrow_{[1]}, \rightarrow_{br, [ > 1 ]})$   
not a loop chart



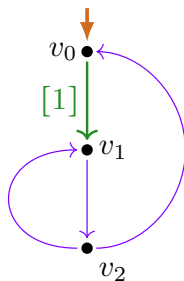
no!

(L2.) violated:

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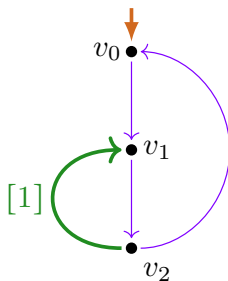
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no!

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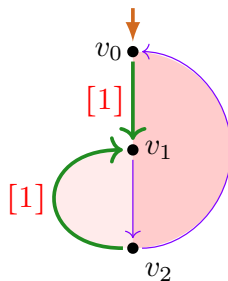
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no!

(L2.) violated:

infinite  $\rightarrow_{br}$  path  
from start vertex

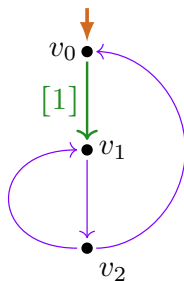


no!

(L3.) violated:

overlapping loop charts  
have **same** level

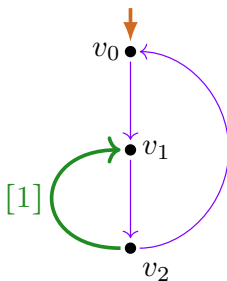
# LEE-witness ?



no!

(L1.) violated:

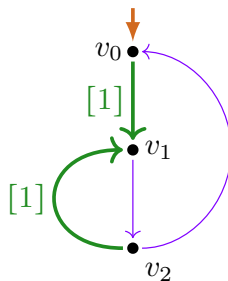
$\mathcal{L}(v_0, \rightarrow_{[1]}, \rightarrow_{br, [ > 1 ]})$   
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no!

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from start vertex



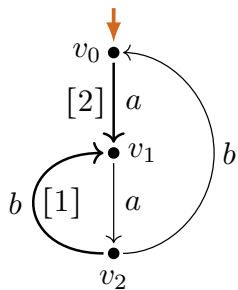
no!

(L3.) violated:

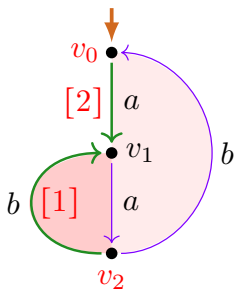
overlapping loop charts  
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# LEE-witness ?



# LEE-witness



$$\mathcal{L}(v_2, \rightarrow_{[1]}, \rightarrow_{br, [ > 1 ]})$$

$$\mathcal{L}(v_0, \rightarrow_{[2]}, \rightarrow_{br, [ > 2 ]})$$

LEE-witness

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## Definition

A loop-branch labeling is a LEE-witness, if:

- L1.  $\forall n \in \mathbb{N} \forall v \in V \left( \mathcal{L}(v, \rightarrow_{[n]}, \rightarrow_{br, [ > n ]}) \right.$   
is a loop subchart, or trivial).
- L2. No infinite  $\rightarrow_{br}$  path from start vertex.
- L3.  $\mathcal{L}(w_i, \rightarrow_{[n_i]}, \rightarrow_{br, [ > n_i ]})$  for  $i \in \{1, 2\}$  loop charts  
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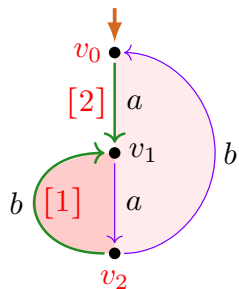
by entry steps  $\rightarrow_{[n]}$  from  $v$

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# Layered LEE-witness



$$\mathcal{L}(v_2, \rightarrow_{[1]}, \rightarrow_{br, [ > 1 ]})$$

$$\mathcal{L}(v_0, \rightarrow_{[2]}, \rightarrow_{br, [ > 2 ]})$$

loop-branch labeling: marking transitions  $\xrightarrow{a}$  as:

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A loop-branch labeling is a layered LEE-witness, if:

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 is a loop subchart

I-L2. No infinite  $\rightarrow_{br}$  path from start vertex.

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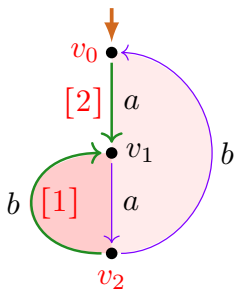
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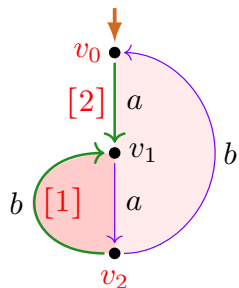
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# Layered LEE-witness



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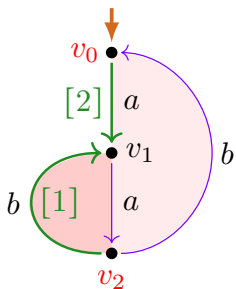
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$$\mathcal{L}(v_2, \rightarrow_{[1]}, \rightarrow_{br})$$

$$\mathcal{L}(v_0, \rightarrow_{[2]}, \rightarrow_{br})$$

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## Definition

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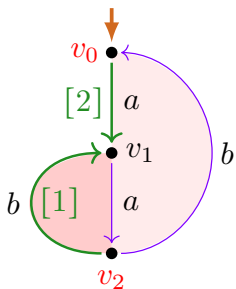
I-L1.  $\forall n \in \mathbb{N} \forall v \in V \left( v \xrightarrow{[n]} \Rightarrow \mathcal{L}(v, \rightarrow_{[n]}, \rightarrow_{br}) \text{ is a loop subchart} \right)$ .

I-L2. No infinite  $\rightarrow_{br}$  path from **start vertex**.

I-L3. A loop subchart generated by a vertex contained in another generated loop subchart has lower level.

$\mathcal{L}(v, \rightarrow_{[n]}, \rightarrow_{br}) :=$  subchart induced  
by entry steps  $\rightarrow_{[n]}$  from  $v$   
followed by branch steps  $\rightarrow_{br}$

# Layered LEE-witness



$$\mathcal{L}(v_2, \rightarrow_{[1]}, \rightarrow_{br})$$

$$\mathcal{L}(v_0, \rightarrow_{[2]}, \rightarrow_{br})$$

layered  
LEE-witness

loop-branch labeling: marking transitions  $\xrightarrow{a}$  as:

- ▶ entry steps  $\xrightarrow{\langle a, [n] \rangle}$  for  $n \in \mathbb{N}$ , written  $\xrightarrow{a}_{[n]}$ ,
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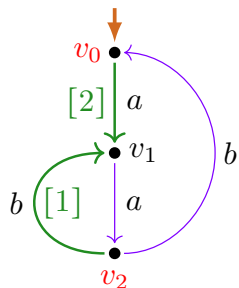
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# LEE versus LEE-witness

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For every process graph  $G$ :

$$\text{LEE}(G) \iff G \text{ has a LEE-witness.}$$

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# LEE versus LEE-witness

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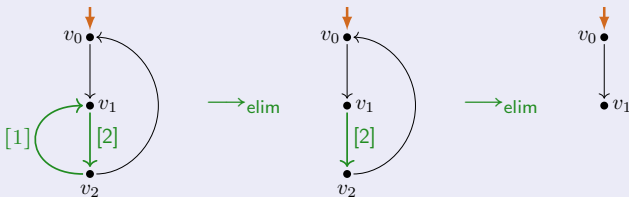
For every process graph  $G$ :

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## Proof.

$\Rightarrow$ : record loop elimination

$\Leftarrow$ : carry out loop-elimination as indicated in the LEE-witness, in *inside-out* direction, e.g.:



# LEE and (layered) LEE-witness

## Lemma

Every layered LEE-witness is a LEE-witness.

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Every LEE-witness  $\widehat{G}$  of a process graph  $G$   
can be transformed by an *effective procedure* (cut-elimination-like)  
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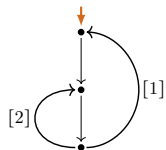
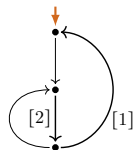
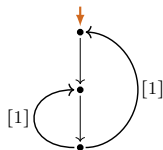
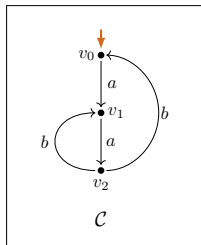
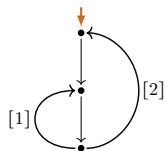
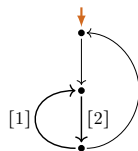
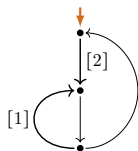
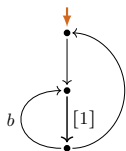
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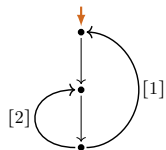
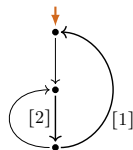
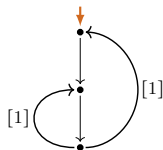
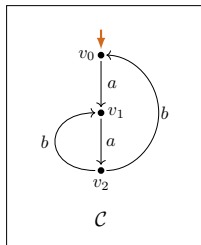
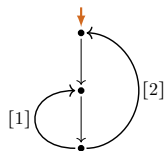
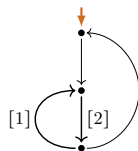
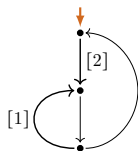
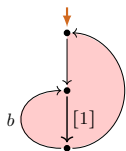
For every process graph  $G$  the following are equivalent:

- (i)  $\text{LEE}(G)$ .
- (ii)  $G$  has a LEE-witness.
- (iii)  $G$  has a *layered* LEE-witness.

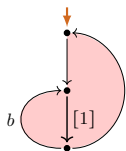
# 7 LEE-witnesses



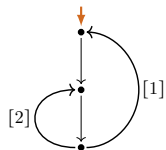
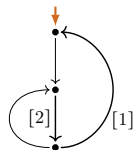
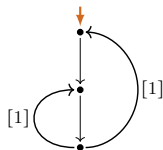
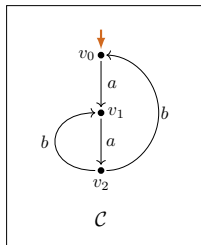
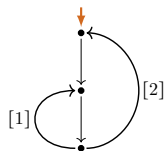
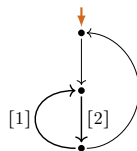
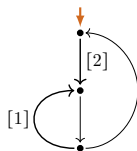
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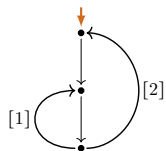
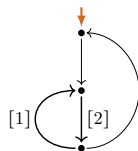
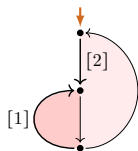
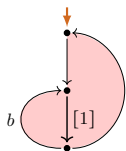


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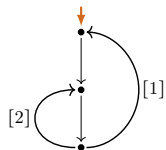
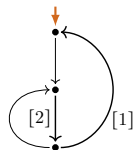
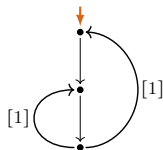
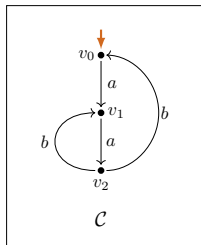




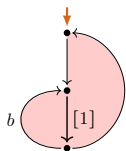
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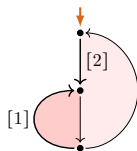
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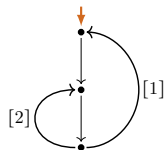
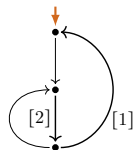
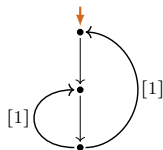
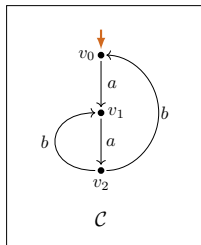
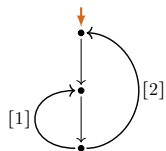
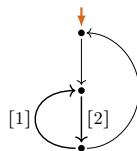
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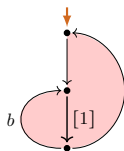
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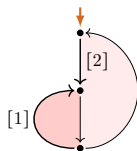
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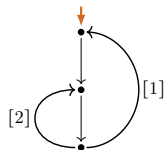
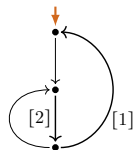
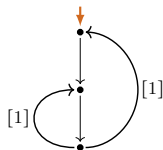
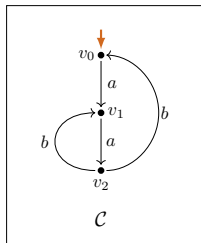
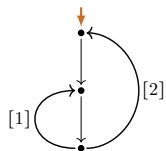
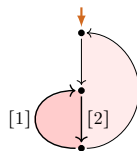
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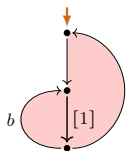
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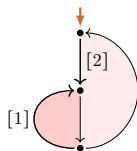
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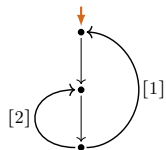
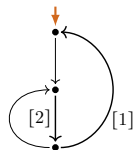
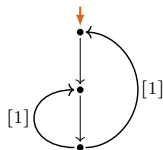
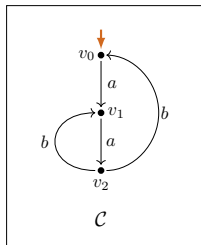
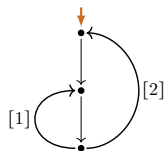
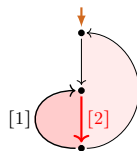
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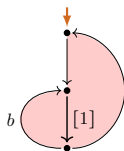
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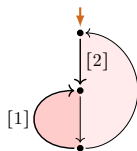
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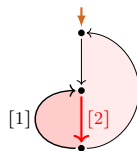
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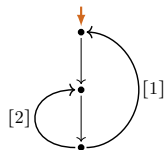
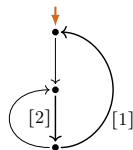
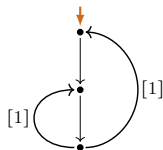
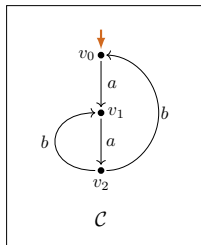
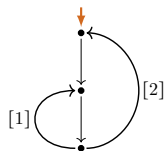
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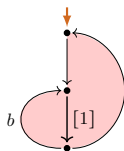
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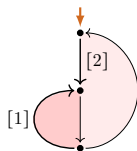
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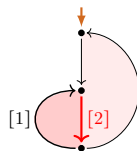
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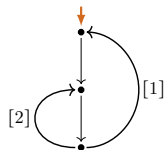
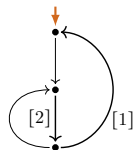
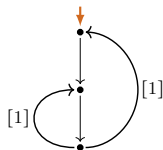
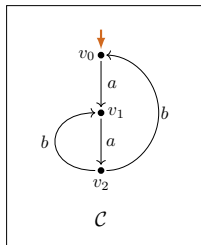
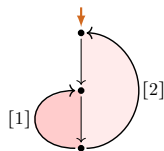
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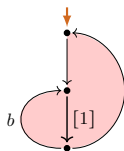
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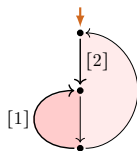
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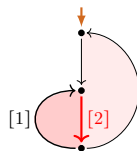
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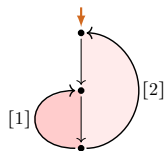
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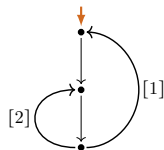
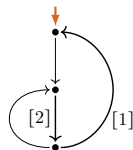
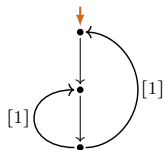
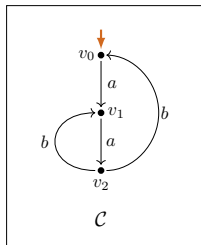
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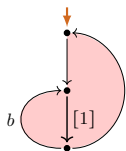
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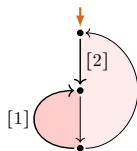
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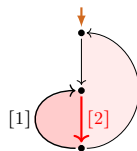
# 7 LEE-witnesses



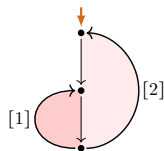
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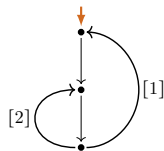
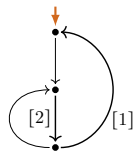
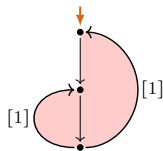
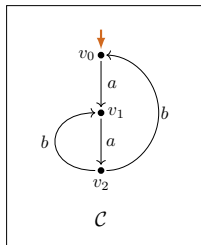
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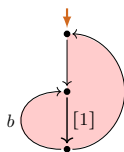


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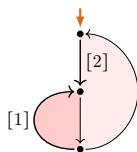




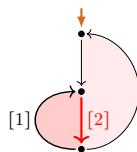
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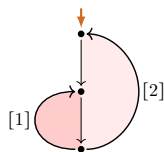
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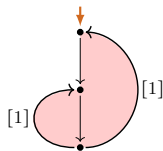
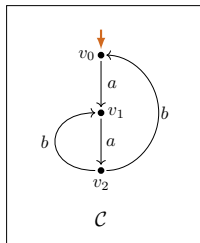
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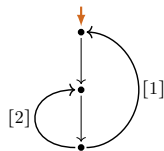
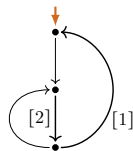
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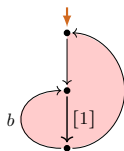
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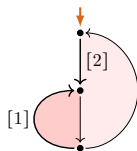
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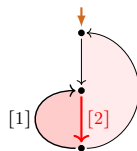
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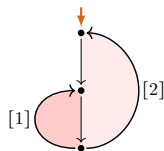
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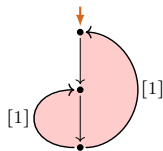
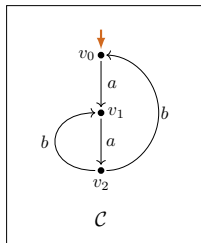
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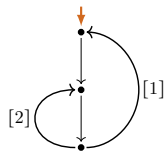
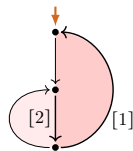
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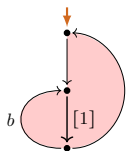
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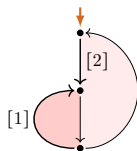
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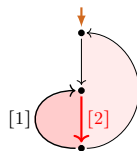
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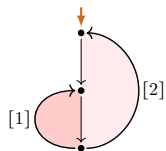
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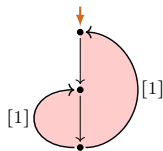
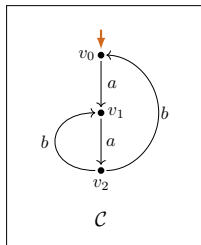
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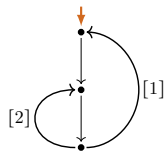
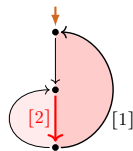
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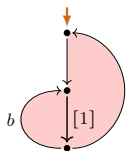
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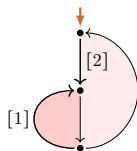
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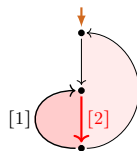
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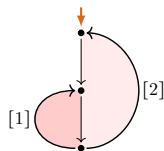
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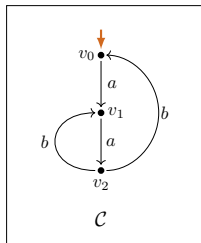
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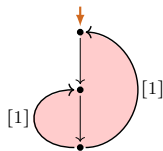
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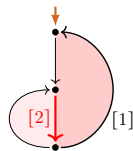
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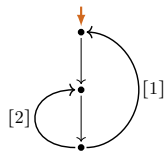
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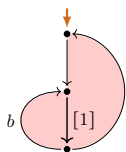
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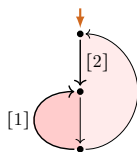
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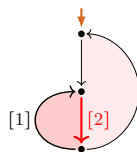
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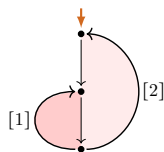
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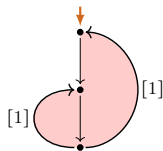
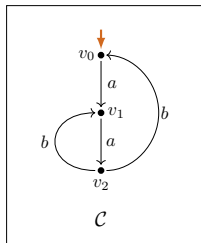
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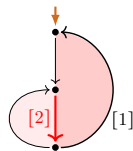
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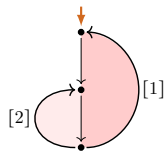
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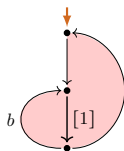
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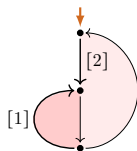
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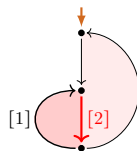
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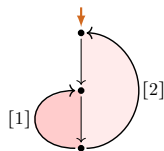
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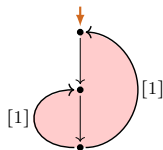
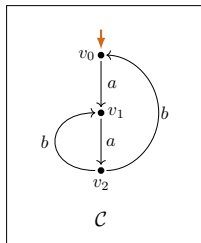
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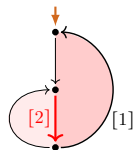
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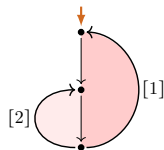
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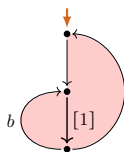


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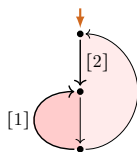


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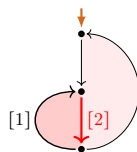
# 7 LEE-witnesses



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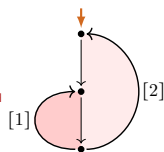


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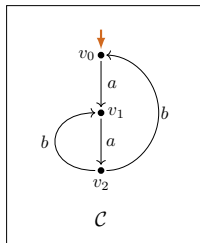


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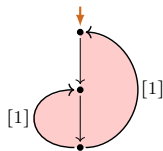
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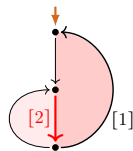
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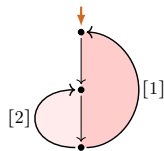
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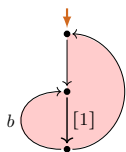


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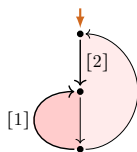


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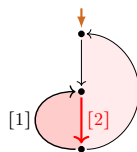
# 7 LEE-witnesses



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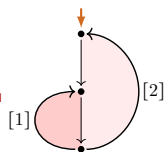


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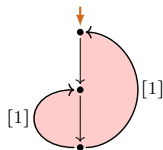
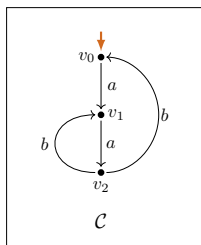


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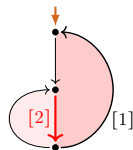
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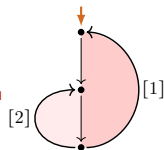


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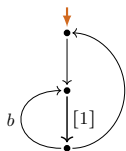
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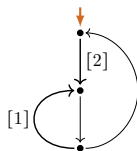
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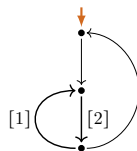
# 7 LEE-witnesses



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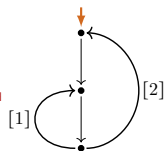


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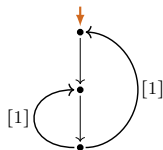
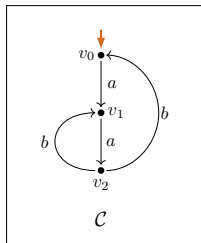


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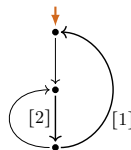
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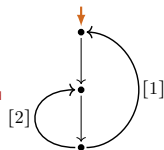


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# LEE under bisimulation?

# LEE under bisimulation

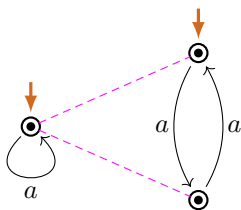
## Observation

- ▶ LEE is **not** invariant under bisimulation.

# LEE under bisimulation

## Observation

- ▶ LEE is **not** invariant under bisimulation.



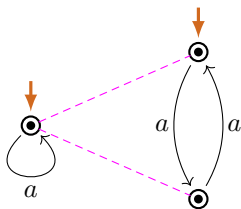
LEE

¬LEE

# LEE under bisimulation

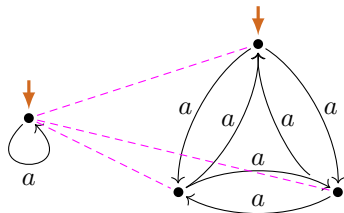
## Observation

- ▶ LEE is **not** invariant under bisimulation.



LEE

¬LEE



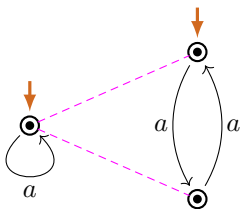
LEE

¬LEE

# LEE under bisimulation

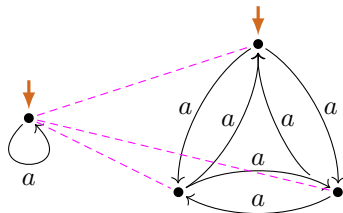
## Observation

- ▶ LEE is **not** invariant under bisimulation.
- ▶ LEE is **not** preserved by converse functional bisimulation.



LEE

¬LEE



LEE

¬LEE

# LEE under functional bisimulation

## Lemma

(i) LEE is preserved by *functional bisimulations*:

$$\text{LEE}(G_1) \wedge G_1 \Rightarrow G_2 \implies \text{LEE}(G_2) .$$

# LEE under functional bisimulation

## Lemma

(i) LEE is preserved by *functional bisimulations*:

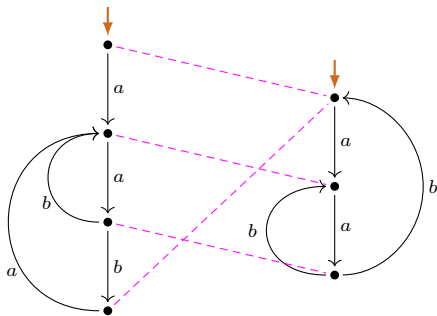
$$\text{LEE}(G_1) \wedge G_1 \Rightarrow G_2 \implies \text{LEE}(G_2) .$$

## Proof (Idea).

Use loop elimination in  $G_1$  to carry out loop elimination in  $G_2$ .

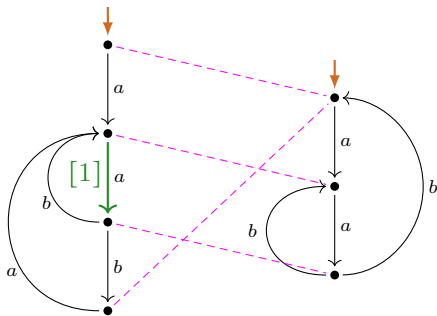


# Collapsing LEE-witnesses



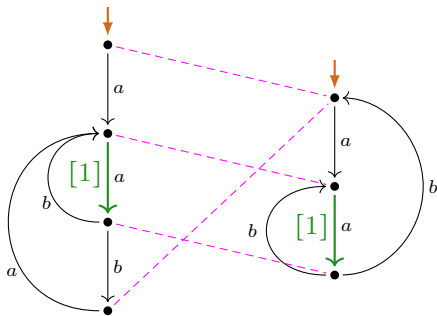
$$\llbracket a(a(b + ba))^*0 \rrbracket_P$$

# Collapsing LEE-witnesses



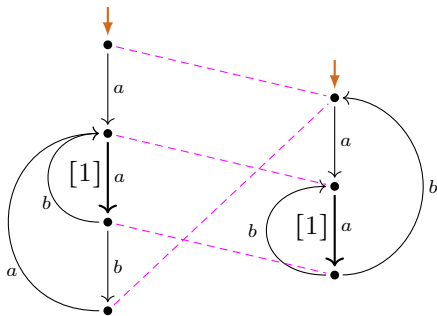
$$\llbracket a(a(b + ba))^*0 \rrbracket_P$$

# Collapsing LEE-witnesses



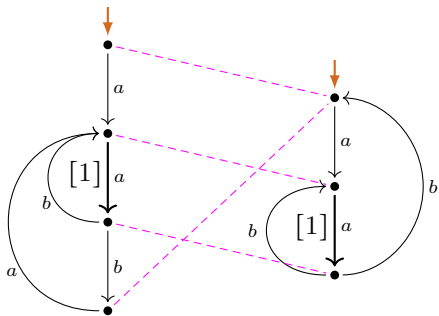
$$\llbracket a(a(b + ba))^*0 \rrbracket_P$$

# Collapsing LEE-witnesses

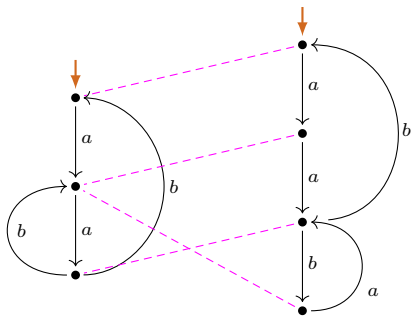


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# Collapsing LEE-witnesses

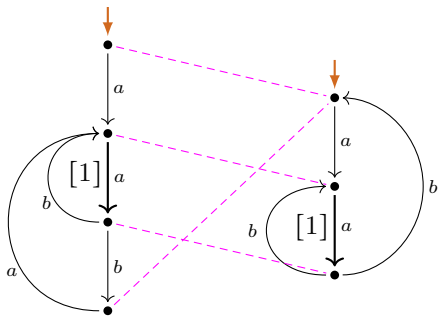


$$\llbracket a(a(b + ba))^*0 \rrbracket_P$$

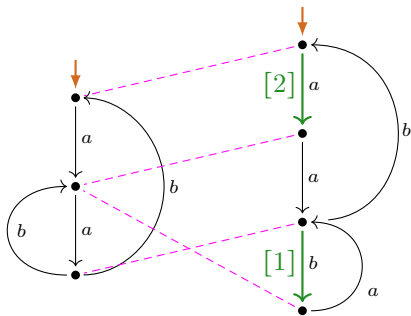


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# Collapsing LEE-witnesses

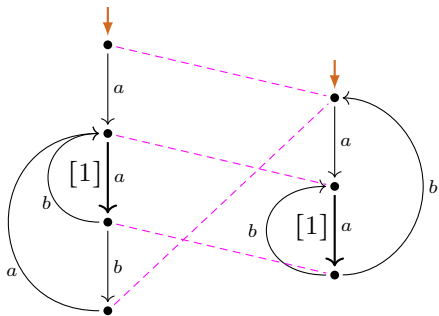


$$\llbracket a(a(b + ba))^*0 \rrbracket_P$$

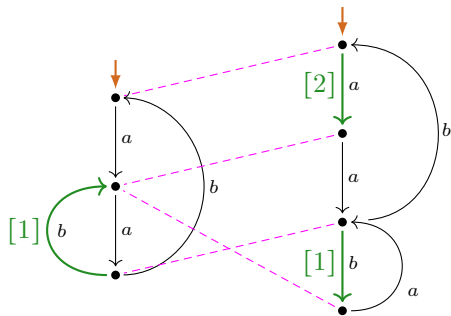


$$\llbracket (aa(ba)^*b)^*0 \rrbracket_P$$

# Collapsing LEE-witnesses

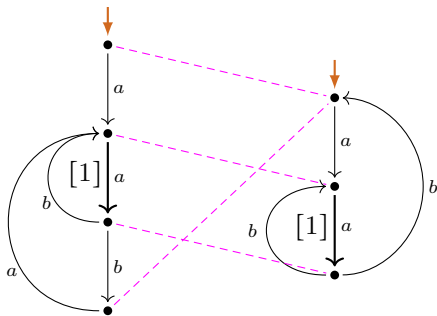


$$\llbracket a(a(b + ba))^*0 \rrbracket_P$$

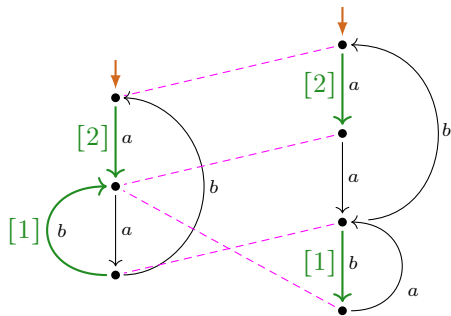


$$\llbracket (aa(ba)^*b)^*0 \rrbracket_P$$

# Collapsing LEE-witnesses



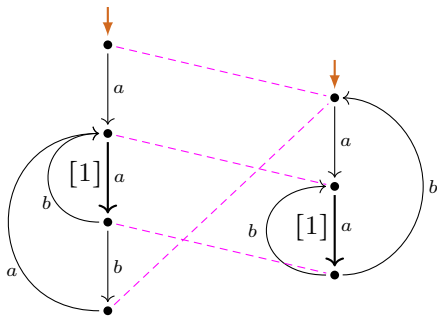
$$\llbracket a(a(b+ba))^*0 \rrbracket_P$$



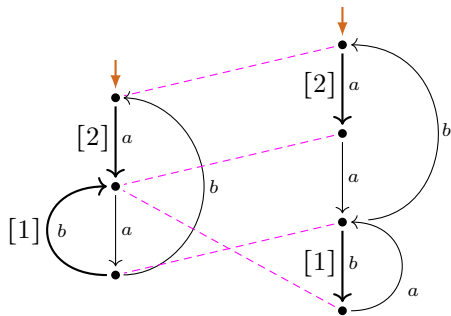
$$\llbracket (aa(ba)^*b)^*0 \rrbracket_P$$



# Collapsing LEE-witnesses



$$\llbracket a(a(b + ba))^*0 \rrbracket_P$$



$$\llbracket (aa(ba)^*b)^*0 \rrbracket_P$$

# LEE under functional bisimulation

## Lemma

(i) LEE is preserved by *functional bisimulations*:

$$\text{LEE}(G_1) \wedge G_1 \Rightarrow G_2 \implies \text{LEE}(G_2) .$$

## Idea of Proof for (i)

Use loop elimination in  $G_1$  to carry out loop elimination in  $G_2$ .

# LEE under functional bisimulation / bisimulation collapse

## Lemma

(i) LEE is preserved by *functional bisimulations*:

$$\text{LEE}(G_1) \wedge G_1 \Rightarrow G_2 \implies \text{LEE}(G_2) .$$

(ii) LEE is preserved from a process graph to its *bisimulation collapse*:

$$\text{LEE}(G) \wedge C \text{ is bisimulation collapse of } G \implies \text{LEE}(C) .$$

## Idea of Proof for (i)

Use loop elimination in  $G_1$  to carry out loop elimination in  $G_2$ .

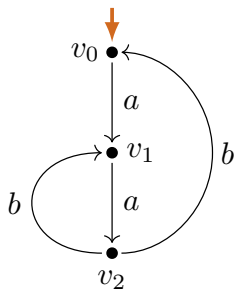
# Readback

## Lemma

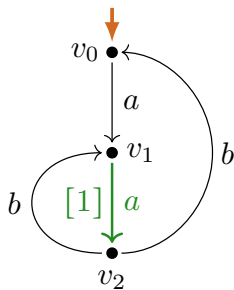
Process graphs with LEE are  $[[\cdot]]_{\mathcal{P}}$ -expressible:

$$\text{LEE}(G) \implies \exists e \in \text{Reg}(A) ( G \Leftrightarrow [[e]]_{\mathcal{P}} ).$$

# Readback from layered LEE-witness (example)

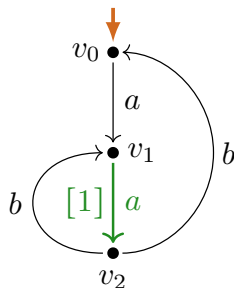


# Readback from layered LEE-witness (example)



layered  
LEE-witness

# Readback from layered LEE-witness (example)



layered  
LEE-witness

$$\begin{aligned}
 s(v_0) &= 0^* \cdot a \cdot s(v_1) \\
 &=_{\text{Mil}} a \cdot s(v_1) \\
 &=_{\text{Mil}} a \cdot (a \cdot (b + b \cdot a))^* \cdot 0
 \end{aligned}$$

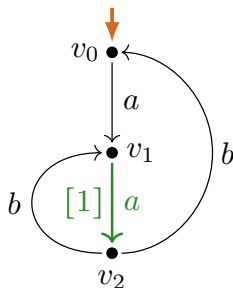
$$\begin{aligned}
 s(v_1) &= (a \cdot s(v_2, v_1))^* \cdot 0 \\
 &=_{\text{Mil}} (a \cdot (b + b \cdot a))^* \cdot 0
 \end{aligned}$$

$$\begin{aligned}
 s(v_2, v_1) &= 0^* \cdot (b \cdot s(v_1, v_1) + b \cdot s(v_0, v_1)) \\
 &=_{\text{Mil}} 0^* \cdot (b \cdot 1 + b \cdot a) \\
 &=_{\text{Mil}} b + b \cdot a
 \end{aligned}$$

$$\begin{aligned}
 s(v_1, v_1) &= 1 \\
 s(v_0, v_1) &= 0^* \cdot a \cdot s(v_1, v_1) \\
 &= 0^* \cdot a \cdot 1 \\
 &=_{\text{Mil}} a
 \end{aligned}$$

# Readback from layered LEE-witness (example)

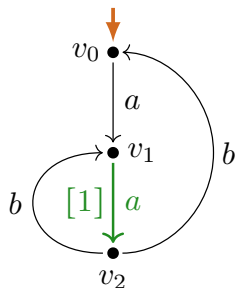
$$s(v_0) = 0^* \cdot a \cdot s(v_1)$$



layered  
LEE-witness



# Readback from layered LEE-witness (example)

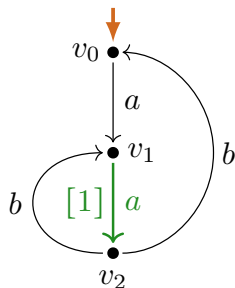


layered  
LEE-witness

$$s(v_0) = 0^* \cdot a \cdot s(v_1)$$

$$s(v_1) = (a \cdot s(v_2, v_1))^* \cdot 0$$

# Readback from layered LEE-witness (example)



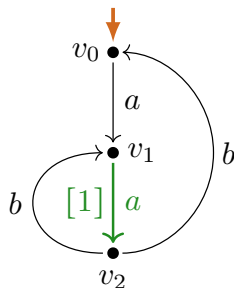
layered  
LEE-witness

$$s(v_0) = 0^* \cdot a \cdot s(v_1)$$

$$s(v_1) = (a \cdot s(v_2, v_1))^* \cdot 0$$

$$s(v_2, v_1) = 0^* \cdot (b \cdot s(v_1, v_1) + b \cdot s(v_0, v_1))$$

# Readback from layered LEE-witness (example)



layered  
LEE-witness

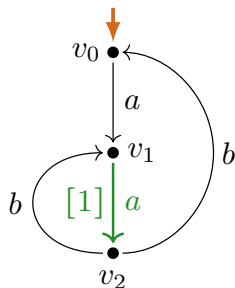
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# Readback from layered LEE-witness (example)



layered  
LEE-witness

$$s(v_0) = 0^* \cdot a \cdot s(v_1)$$

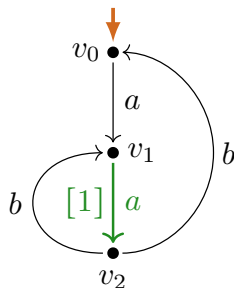
$$s(v_1) = (a \cdot s(v_2, v_1))^* \cdot 0$$

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$$s(v_1, v_1) = 1$$

$$s(v_0, v_1) = 0^* \cdot a \cdot s(v_1, v_1)$$

# Readback from layered LEE-witness (example)



layered  
LEE-witness

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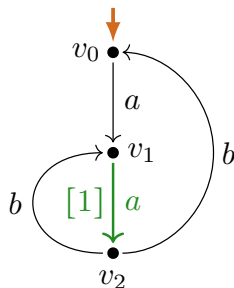
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$$s(v_1, v_1) = 1$$

$$s(v_0, v_1) = 0^* \cdot a \cdot s(v_1, v_1)$$

$$= 0^* \cdot a \cdot 1$$

# Readback from layered LEE-witness (example)



layered  
LEE-witness

$$s(v_0) = 0^* \cdot a \cdot s(v_1)$$

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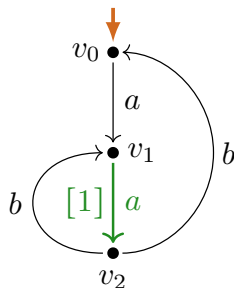
$$s(v_1, v_1) = 1$$

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$$= 0^* \cdot a \cdot 1$$

$$= \text{Mil} \cdot a$$

# Readback from layered LEE-witness (example)



layered  
LEE-witness

$$s(v_0) = 0^* \cdot a \cdot s(v_1)$$

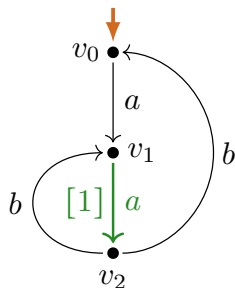
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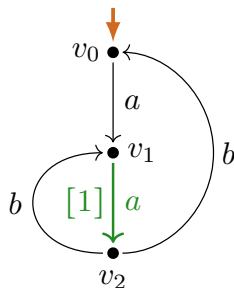
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# Readback from layered LEE-witness (example)



layered  
LEE-witness

$$s(v_0) = 0^* \cdot a \cdot s(v_1)$$

$$s(v_1) = (a \cdot s(v_2, v_1))^* \cdot 0$$

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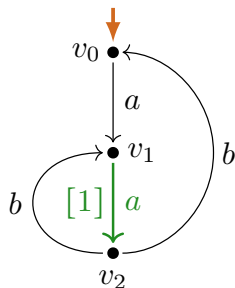
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$$=_{\text{Mil}} a$$

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layered  
LEE-witness

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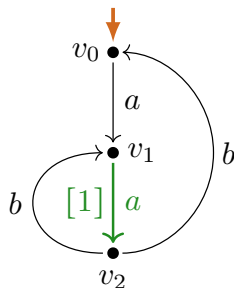
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# Readback from layered LEE-witness (example)



layered  
LEE-witness

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# 1-return-less regular expressions

## Lemma

Process graphs with LEE are  $\llbracket \cdot \rrbracket_P$ -expressible:

$$\text{LEE}(G) \implies \exists e \in \text{Reg}(A) ( G \Leftrightarrow \llbracket e \rrbracket_P ).$$

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Process graphs with LEE are  $\llbracket \cdot \rrbracket_P^{1r^*}$ -expressible:

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Definition (Corradini, De Nicola, Labella (here intuitive version))

A regular expression  $e$  is 1-return-less(-under- $\star$ ) ( $e \in \text{Reg}^{1r^*}(A)$ ) if:

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  - ▶  $p$  has the option to **immediately terminate**, **and**
  - ▶  $p$  has the option to **do a proper step**, and terminate later.

## Non-/Examples of 1-return-less regular expressions

- ▶  $(a \cdot (1 + b))^*$

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- ▶  $(a \cdot (0^* + b))^*$

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- ▶  $(a \cdot (0^* + b))^*$  ✘

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## Non-/Examples of 1-return-less regular expressions

- ▶  $(a \cdot (1 + b))^*$  ✘
- ▶  $(a \cdot (0^* + b))^*$  ✘
- ▶  $a \cdot (a \cdot (b + b \cdot a))^* \cdot 0$

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## Non-/Examples of 1-return-less regular expressions

- ▶  $(a \cdot (1 + b))^*$  ✘
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# Characterization of expressibility<sup>1r\\*</sup> modulo $\Leftrightarrow$

## Theorem

For every process graph  $G$  with bisimulation collapse  $C$  the following are equivalent:

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Also yields: efficient decision method of  $\llbracket \cdot \rrbracket_P^{1r\*}$ -expressibility modulo  $\Leftrightarrow$ .

# Structure constrained finite process graphs

graphs with LEE / a (layered) LEE-witness

*Benefits* of the class of process graphs with LEE:

- ▶ is closed under  $\Rightarrow$
- ▶ forth-/back-correspondence with 1-return-less regular expressions

# Structure constrained finite process graphs

- graphs with LEE / a (layered) LEE-witness
- $\not\subseteq$  graphs whose collapse satisfies LEE
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*Application to Milner's questions* yields partial results:

Q1: characterization/efficient decision of  $[[\cdot]]_{\mathcal{P}}^{1r^*}$ -expressibility modulo  $\Leftrightarrow$

Q2: alternative compl. proof of Mil on 1-return-less expressions (C/DN/L)



# Comparison results: structure-constrained graphs

$\lambda$ -calculus with letrec under  $=_{\lambda^\infty}$

*Not available:* graph interpretation that is studied under  $\Leftrightarrow$

Regular expressions under  $\Leftrightarrow_P$

*Given:* graph interpretation  $\llbracket \cdot \rrbracket_P$ , studied under bisimulation  $\Leftrightarrow$

- ▶ not closed under  $\Rightarrow$ , and  $\Leftrightarrow$ , incomplete under  $\Leftrightarrow$

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- ▶ closed under  $\Rightarrow$  (hence under collapse)
- ▶ back-/forth correspondence with  $\lambda$ -calculus with letrec
  - ▶ efficient translation and readback
  - ▶ translation is inverse of readback

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*Defined:* class of process graphs with LEE / (layered) LEE-witness

- ▶ closed under  $\Rightarrow$  (hence under collapse)
- ▶ back-/forth correspondence with 1-return-less expr's
- ▶ contains the collapse of a process graph  $G$ 
  - $\iff G$  is  $\llbracket \cdot \rrbracket_P^{\dagger \dagger \dagger}$ -expressible modulo  $\Leftrightarrow$